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2	2	H6P	JTAG,USB,PLL,HSIC,XTAL	N/A	N/A		
3	3	H6P	DIGITAL I/O,BOOTSTRAPPING	N/A	N/A		
4	4	H6P	VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU	N/A	N/A		
5	5	H6P	GND,VDDIO18,VDDIOD,VDD_SRAM,VDD_SOC	N/A	N/A		
6	6	H6P	NAND,NAND 12X17	N/A	N/A		
7	7	H6P	HIGH SPEED DIG (CAM,LCM,DP)	N/A	N/A		
8	8		BUTTON FLEX B2B	N/A	N/A		
9	9	L67	AUDIO CODEC (1/2)	N/A	N/A		
10	10	L67	AUDIO CODEC (2/2)	N/A	N/A		
11	11		FRONT CAM FLEX B2B	N/A	N/A		
12	12	AMBER	PMU(1/2)	N/A	N/A		
13	13	AMBER	PMU(2/2)	N/A	N/A		
14	14	CHESTNUT	BACKLIGHT DRIVER,MESA BOOST	N/A	N/A		
15	15	SPKR	AMP + STROBE DRIVER	N/A	N/A		
16	16	TRISTAR	,EEPROM	N/A	N/A		
17	17		DOCKFLEX B2B	N/A	N/A		
18	18	D403	(TOUCH B2B, DRIVER ICS)	N/A	N/A		
19	19	LCM	B2B	N/A	N/A		
20	20	OSCAR	+ SENSORS	N/A	N/A		
21	21	REAR	CAM B2B	N/A	N/A		
22	22	BATT	B2B, TPS, PD FEATURES	N/A	N/A		
23	23		VOLTAGE PROPERTIES				
24	24	RADIO_MLB	HIERARCH. SYMBOL	N/A	N/A		
25	25		Cross Reference Page				
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27	27		Cross Reference Page				

SCH 051-9681
BRD 820-3382
MCO 056-5179
BOM 639-4159
BOM 639-4160
BOM 639-3973

{ 16GB }
{ 32GB }
{ 64GB }

X152
X152
X152

COMPASS BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
639-4269	1	COMPASS INTERPOSER X152/X145	U16	Y	COMPASS_INTERPOSER

HORIZONTAL AND OTHER CAP BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
138S0801	5	HRZNTL CAPS_1: 10UF,0402,6.3V	C422,C399,C405,C417,C418	Y	HRZNTL_CAP_GRP1
138S0801	5	HRZNTL CAPS_2: 10UF,0402,6.3V	C250,C251,C325,C357,C358	Y	HRZNTL_CAP_GRP2
138S0801	5	HRZNTL CAPS_3: 10UF,0402,6.3V	C260,C263,C267,C270,C261	Y	HRZNTL_CAP_GRP3
138S0801	4	HRZNTL CAPS_4: 10UF,0402,6.3V	C264,C268,C271,C385	Y	HRZNTL_CAP_GRP4
138S0801	4	HRZNTL CAPS_5: 10UF,0402,6.3V	C398,C411,C252,C297	Y	HRZNTL_CAP_GRP5
138S0801	5	HRZNTL CAPS_6: 10UF,0402,6.3V	C386,C387,C333,C332,C335	Y	HRZNTL_CAP_GRP6
138S0801	3	HRZNTL CAPS_7: 10UF,0402,6.3V	C42_RF,C43_RF,C44_RF	Y	HRZNTL_CAP_GRP7
138S0801	1	HRZNTL CAPS_8: 10UF,0402,6.3V	C1281_RF	Y	HRZNTL_CAP_GRP8
138S0801	1	HRZNTL CAPS_9: 10UF,0402,6.3V	C103_RF	Y	HRZNTL_CAP_GRP9
138S0801	4	HRZNTL CAPS_10: 10UF,0402,6.3V	C182,C307,C209,C187	Y	HRZNTL_CAP_GRP10
138S0794	2	HRZNTL CAPS_11: 10UF,0402,10V	C52,C156	Y	HRZNTL_CAP_GRP11

INDUCTOR BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
152S1785	3	BUCK0 SLAVE IND: 0.47UH, TFA-A TDK	L10,L12,L14	Y	IND_BUCK0_SLV_P47UH_TFA-A_TDK
152S1834	3	BUCK0 SLAVE IND: 0.47UH, CYNTEC	L10,L12,L14	Y	IND_BUCK0_SLV_P47UH_CYNTEC
152S1839	3	BUCK0 SLAVE IND: 0.47UH, TAIYO	L10,L12,L14	Y	IND_BUCK0_SLV_P47UH_TAIYO
152S1807	6	AMBER BUCKXX IND: 1UH TFA-A TDK	L9,L11,L13,L15,L16,L17	Y	IND_BUCKXX_1UH_TFA-A_TDK
152S1801	6	AMBER BUCKXX IND: 1UH CYNTEC	L9,L11,L13,L15,L16,L17	Y	IND_BUCKXX_1UH_CYNTEC
152S1840	6	AMBER BUCKXX IND: 1UH TAIYO	L9,L11,L13,L15,L16,L17	Y	IND_BUCKXX_1UH_TAIYO
152S1807	1	STROBE IND: 1UH TFA-A TDK	L5	Y	IND_STROBE_1UH_TFA-A_TDK
152S1801	1	STROBE IND: 1UH CYNTEC	L5	Y	IND_STROBE_1UH_CYNTEC
152S1840	1	STROBE IND: 1UH TAIYO	L5	Y	IND_STROBE_1UH_TAIYO
152S1809	1	BUCK5 2012 IND: 1UH TFA-A TDK	L18	Y	IND_BUCK5_1UH_TFA-A_TDK
152S1835	1	BUCK5 2012 IND: 1UH CYNTEC	L18	Y	IND_BUCK5_1UH_CYNTEC
152S1843	1	BUCK5 2012 IND: 1UH TAIYO	L18	Y	IND_BUCK5_1UH_TAIYO
152S1836	1	SPKR AMP IND: 1.2UH CYNTEC	L4	Y	IND_SPKRAMP_1P2UH_CYNTEC
152S1844	1	SPKR AMP IND: 1.2UH TAIYO	L4	Y	IND_SPKRAMP_1P2UH_TAIYO
152S1721	1	CHARGER IND: 2.2UH TAIYO	L8	Y	IND_CHGR_2P2UH_TAIYO

FOR CHESTNUT BOMTABLE - SEE PG 14
FOR RADIO BOMTABLE - SEE PG 24
FOR MISC R/L/C ALTS - SEE PG 2

I2C ADDRESS MAP

I2C0	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
	AMBER PMU:	11010100X	0X74	0XE8
	CS35L19B AMP:	10000000X	0X40	0X80
	LM3534 BL DRIVER:	11000111X	0X63	0XC6
	TRISTAR:	00110101X	0X1A	0X34
	CHESTNUT:	01001111X	0X27	0X4E
I2C1	CT814 ALS:	01010011X	0X29	0X52
RCAM I2C	OPEL STROBE DRIVER:	11000111X	0X63	0XC6
	REAR FACING CAM:	00100000X	0X10	0X20
	ADI VCM AF DRIVER:	00011101X	0X0E	0X1C
	ROHM VCM AF DRIVER:	00011001X	0X0C	0X18
FCAM I2C	FRONT FACING CAM:	01101101X	0X36	0X6C

NOTE: ACCEL, GYRO, COMPASS ALL USING SPI (VIA OSCAR) FOR AP COMMUNICATION.

X152 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-9681	1	SCH, SINGLE_BRD, X152	SCH	Y	?
820-3382	1	PCBF, SINGLE_BRD, X152	PCB	Y	?
825-6838	1	EEEE FOR 639-4159 16GB	EEEE_F7V1	Y	EEEE_16G
825-6838	1	EEEE FOR 639-4160 32GB	EEEE_F7V2	Y	EEEE_32G
825-6838	1	EEEE FOR 639-3973 64GB	EEEE_F4LR	Y	EEEE_64G
339S0206	1	H6P + 1GB SAMSUNG	U1	Y	H6P_1GB_SAMSUNG
339S0207	1	H6P + 1GB ELPIDA	U1	Y	H6P_1GB_ELPIDA
339S0208	1	H6P + 1GB HYNIX	U1	Y	H6P_1GB_HYNIX

OSCAR BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
337S4370	1	OSCAR CSP	U9	Y	OSCAR_CSP
337S4417	1	OSCAR FCLGA	U9	Y	OSCAR_FCLGA

OPEL BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
353S3899	1	TI OPEL	U17	Y	OPEL_TI

NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0930	1	NAND,19NM,16GX8,MLC,PPN1.5	U4	Y	NAND_16G_HYNIX
335S0931	1	NAND,19NM,32GX8,MLC,PPN1.5	U4	Y	NAND_32G_HYNIX
335S0932	1	NAND,19NM,64GX8,MLC,PPN1.5	U4	Y	NAND_64G_HYNIX

NAND BOM ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0921	335S0930	NAND_16G_TOSH	U4	?
335S0933	335S0930	NAND_16G_SAND	U4	?
335S0922	335S0931	NAND_32G_TOSH	U4	?
335S0934	335S0931	NAND_32G_SAND	U4	?
335S0923	335S0932	NAND_64G_TOSH	U4	?
335S0935	335S0932	NAND_64G_SAND	U4	?

USB GOLDENEYE BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
155S0583	2	E75 COMMON MODE CHOKES	L20,L22	Y	CMC_E75_D1FFPFAIRS
152S1737	2	USB TX 10UH SERIES INDUCTORS	R163,R164	Y	USB_TX_SERIES_IND

TRISTAR BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
343S0614	1	CBTL1608A1UK,WCSF,TRISTAR	U2	Y	TRISTAR
343S0639	1	CBTL1610A0UK,WCSF,TRISTAR2	U2	Y	TRISTAR2
117S0202	2	RES 20OHM 01005 5%, TRISTAR2	R102,R103	Y	TRISTAR2
118S0671	2	RES 15OHM 01005 5%, TRISTAR	R102,R103	Y	TRISTAR

AUDIO BOM OPTION

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
155S0556	2	FERRITE 0402 P14OHM 1A	FL6, FL9	Y	SPKAMP_FERRITE_REG
155S0731	2	FERRITE 0402 P060HM 1P8A	FL6, FL9	Y	SPKAMP_FERRITE_LOWDCR
116S0004	2	RESISTOR 0402 00HM 1A	FL6, FL9	Y	SPKAMP_FERRITE_00HM
132S0396	2	CAP 01005 10V 1000PF	C500, C501	Y	SPKAMP_CAPFILT_1000PF
132S0437	2	CAP 01005 10V 150PF	C500, C501	Y	SPKAMP_CAPFILT_150PF
131S0283	2	CAP 01005 10V 100PF	DZ13, DZ14	Y	SPKAMP_ESDFILT_100PF
338S1077	1	CLASSD AMP, L19	U22	Y	SPKAMP_IC_L19
338S1161	1	CLASSD AMP, L20	U22	Y	SPKAMP_IC_L20
117S0002	1	0201 00HM	R128	Y	SPKAMP_SENSE_R_L20
118S0583	1	0201 0.10HM	R128	Y	SPKAMP_SENSE_R_L19

D

DC

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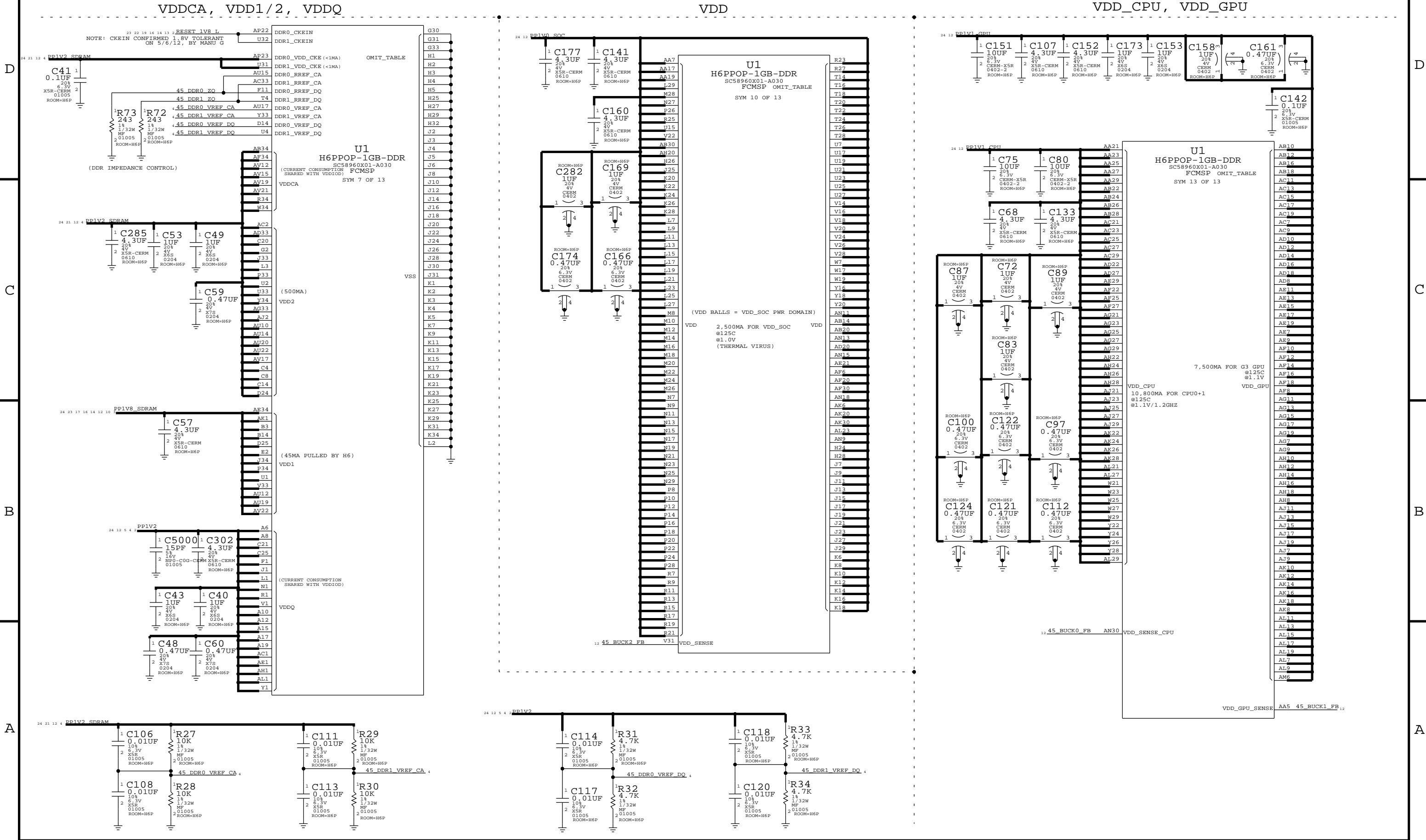
A

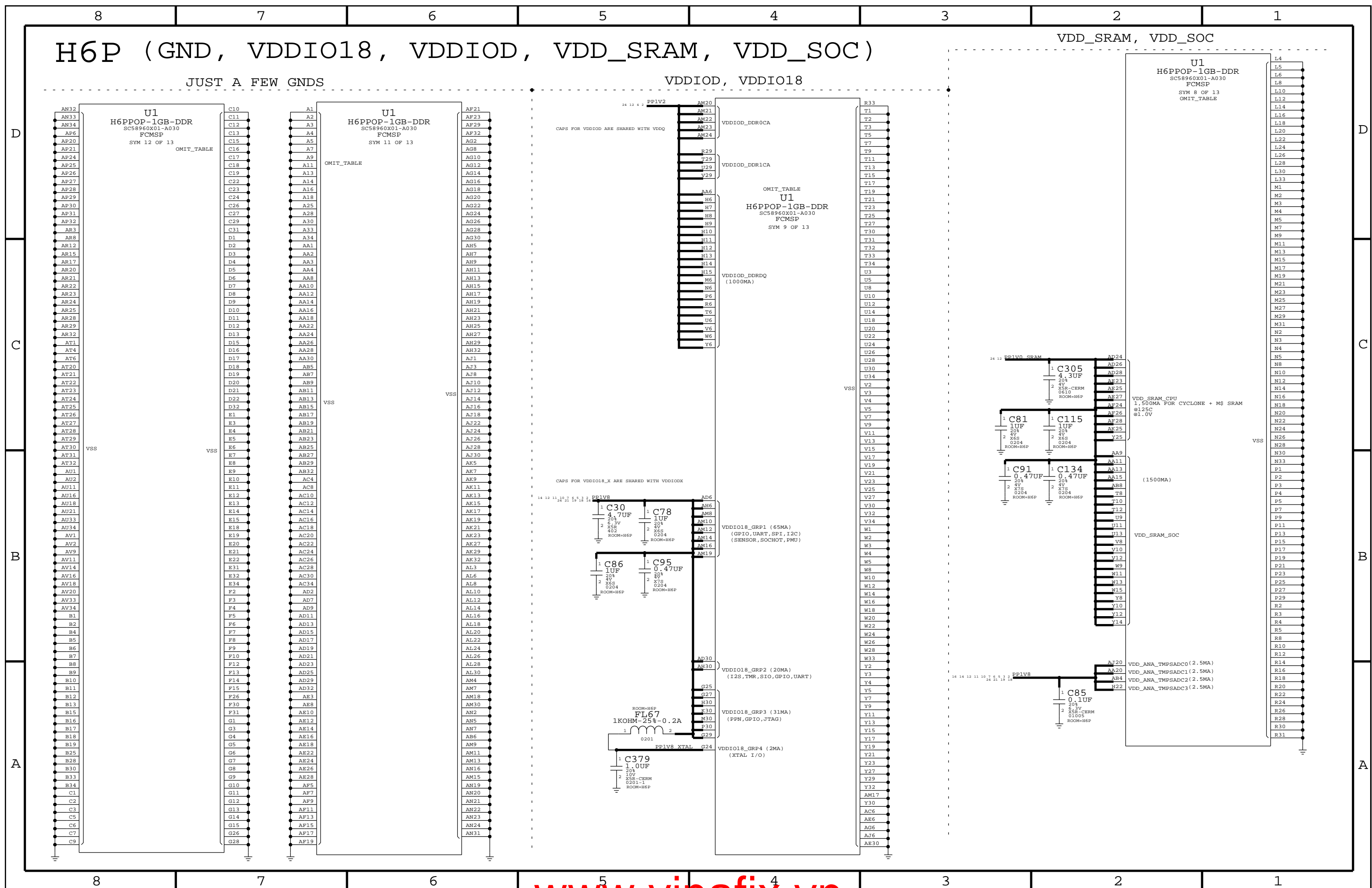
PCB: PLACE THIS TOP SIDE
NORTH END OF SINGLE_BRD

PCB: PLACE THIS BOTTOM SIDE
SOUTH END OF SINGLE_BRD



H6P: GND, VDDCA, VDD1/2, VDD, VDD_CPU, VDD_GPU

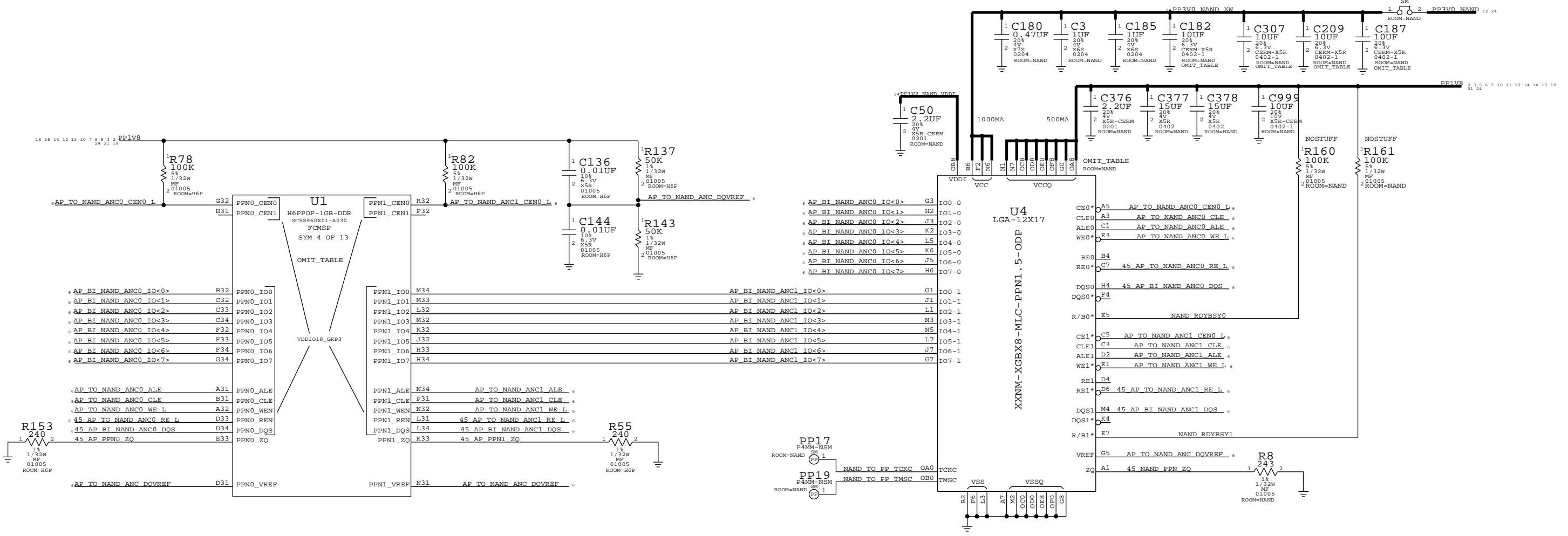




H6P NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY

PCB: THIS XW ON OUTER LAYER, ACCESSIBLE FOR REWORK



NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

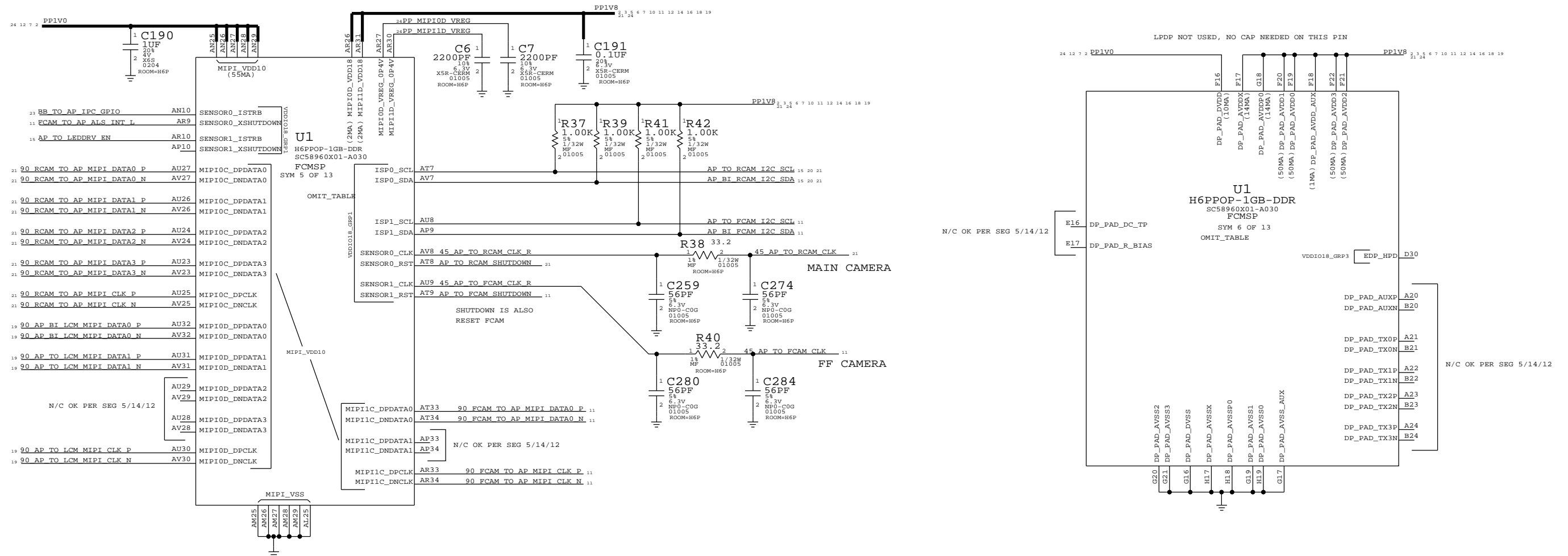
NOTE: IO<6> PREFERRED BY MATT BYOM
(IS A STATUS READY BIT)

PP2
P4MM-NSM
ROOM=H6P
1 45 AP BI NAND ANCO IO<6>

PP3
P4MM-NSM
ROOM=H6P
1 45 AP TO NAND ANCO RE L

PP10
P4MM-NSM
ROOM=H6P
1 45 AP BI NAND ANCO DQS

H6P HIGH SPEED DIG (CAM, LCD, DP)



8 7 6 5 4 3 2 1

BUTTON FLEX (VIBE DRIVER, BUTTONS, ANC REF MIC, STROBE, STROBE_NTC)

STROBE:
LED WARM, RETURN

WIFI FLEX PAC:
VDD (3.0V)

VIBE DRIVE

BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN

STROBE:
STROBE NTC

MIC2 (ANC REF MIC):
MIC2/3 BIAS,
MIC2_P,_N

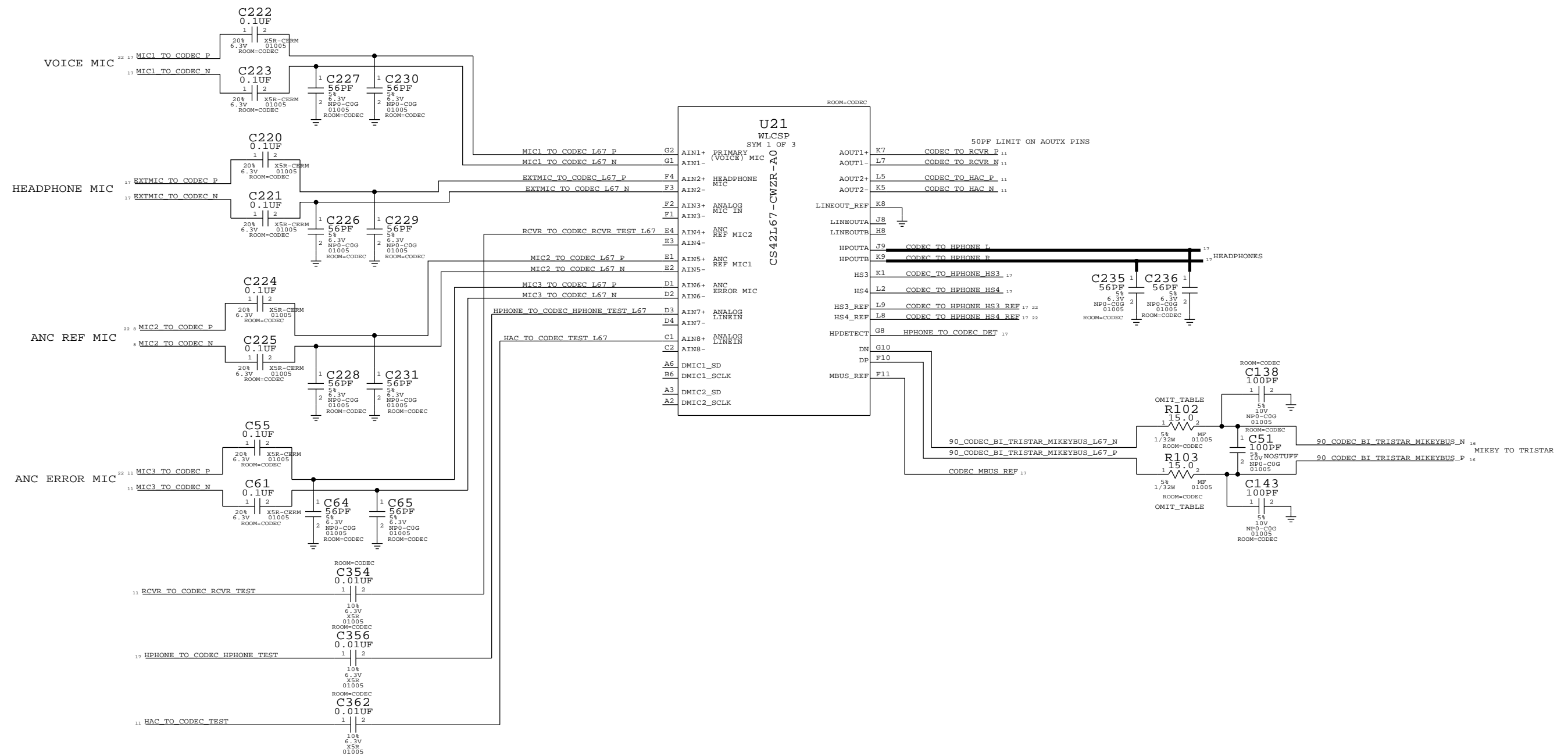
WIFI FLEX PAC:
PAC SPI BUS

STROBE:
LED COOL

L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



D

C

C



8	7	6	5	4	3	2	1
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RECEIVER

J1 ROOM=CG_B2B
AA22L-S034VA1
F-ST-SM

20% SPECIAL Z = 0.60 MM MAX
6.3V
X5R
402
ROOM=CG_B2B

PROX: PWR, TX EN

D



```
(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)
```

D

D

C

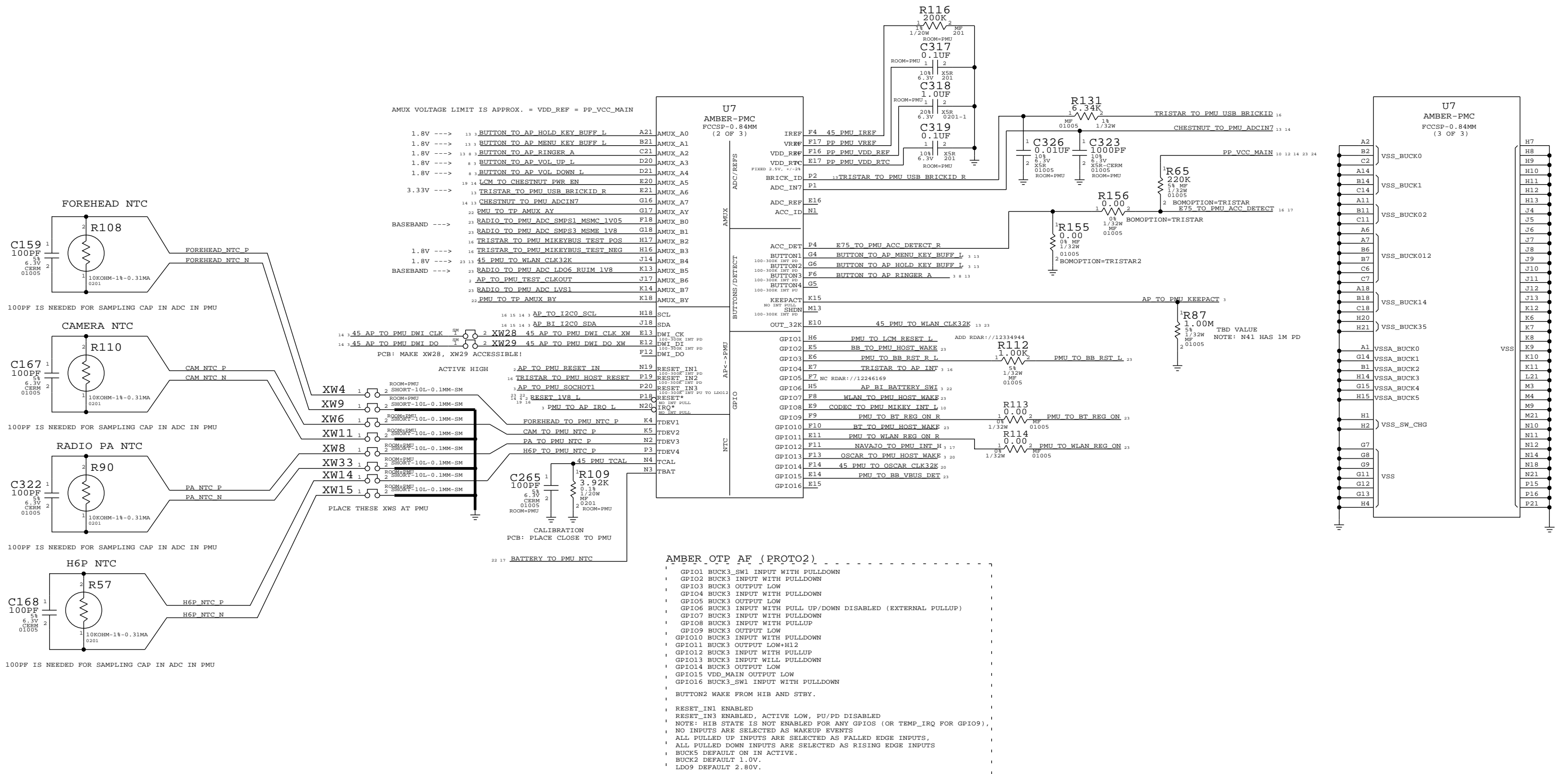
C

B

B

A

A

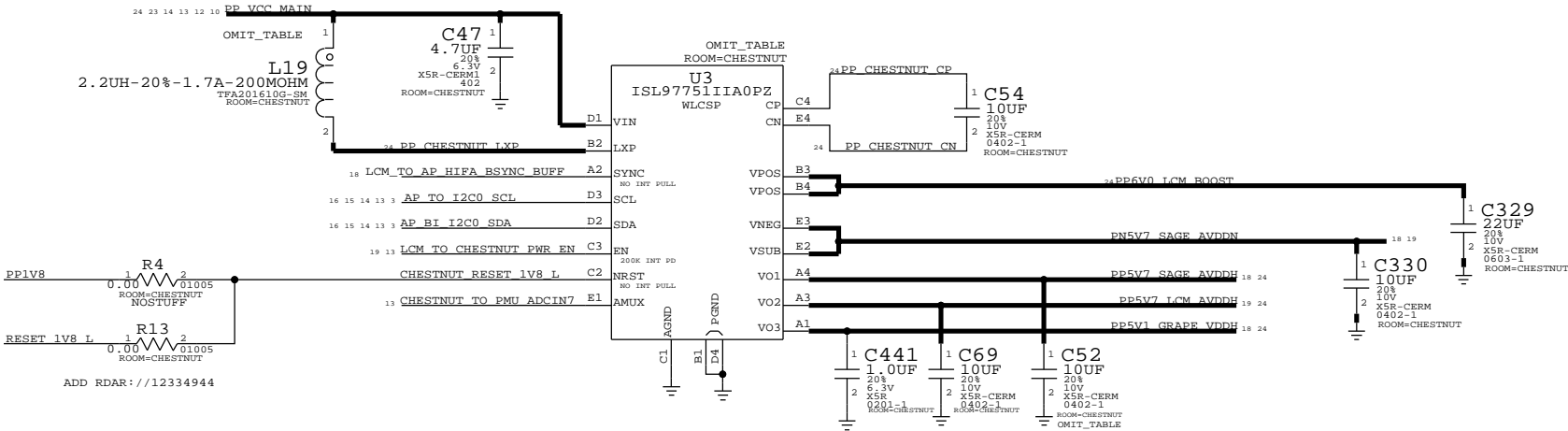


CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

CHESTNUT BOM OPTIONS

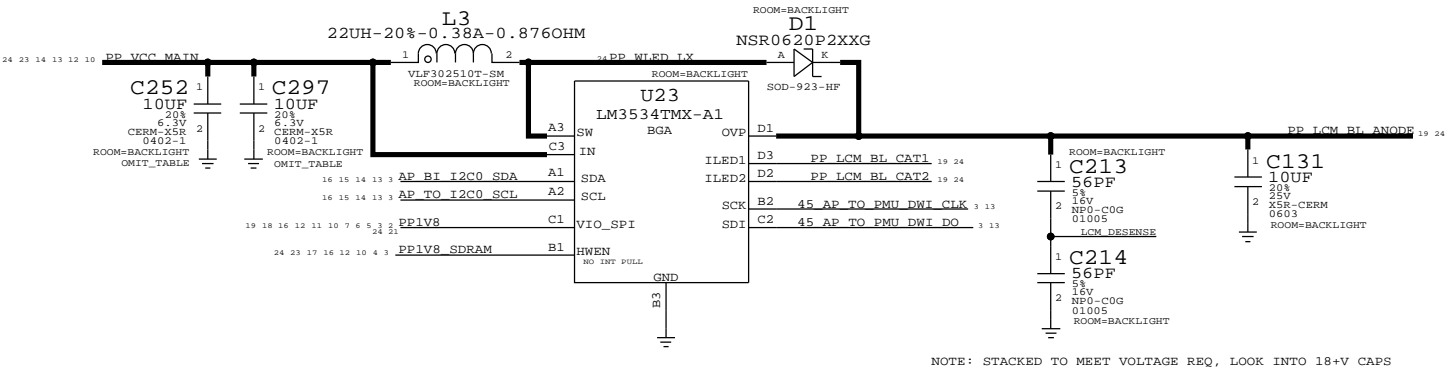
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S1172	1	TI CHESTNUT	U3	Y	CHESTNUT_TI
152S1842	1	TI CHESTNUT IND - 1.5UH TAIYO	L19	Y	CHESTNUT_TI_TAIYO
152S1802	1	TI CHESTNUT IND - 1.5UH CYNTEC	L19	Y	CHESTNUT_TI_CYNTEC
338S1168	1	INTERSIL CHESTNUT	U3	Y	CHESTNUT_INTERSIL
152S1805	1	INTERSIL CHESTNUT IND - 2.2UH TFA-A	L19	Y	CHESTNUT_INTERSIL_TFA-A

D403 DISPLAY PMU (INTERSIL CHESTNUT, 338S1148)
(TI CHESTNUT, 338S1149)



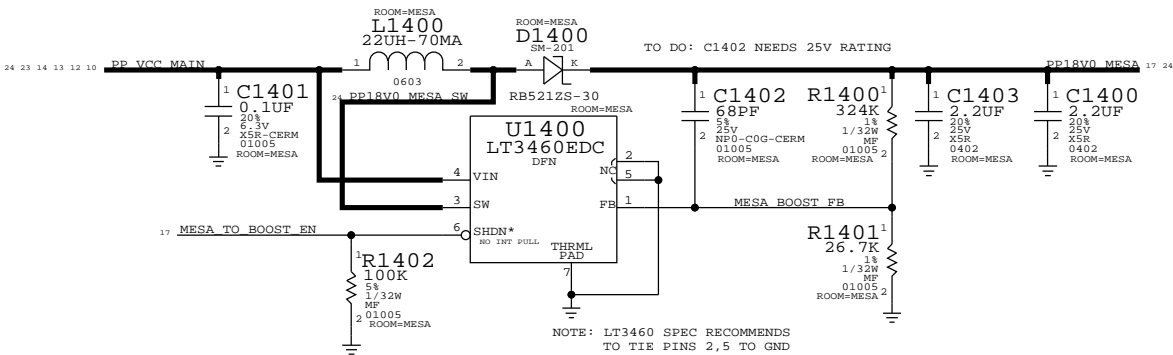
SAGE NEG BOOST TIMING INFO:
2 MS NOMIAL START UP DELAY FOR LCM POWER SEQUENCING
0 MS DELAY AT SHUTDOWN
ACTIVE DISCHARGE 2MS TO RAIL DOWN

D403 BACKLIGHT DRIVER



NOTE: STACKED TO MEET VOLTAGE REQ, LOOK INTO 18+V CAPS

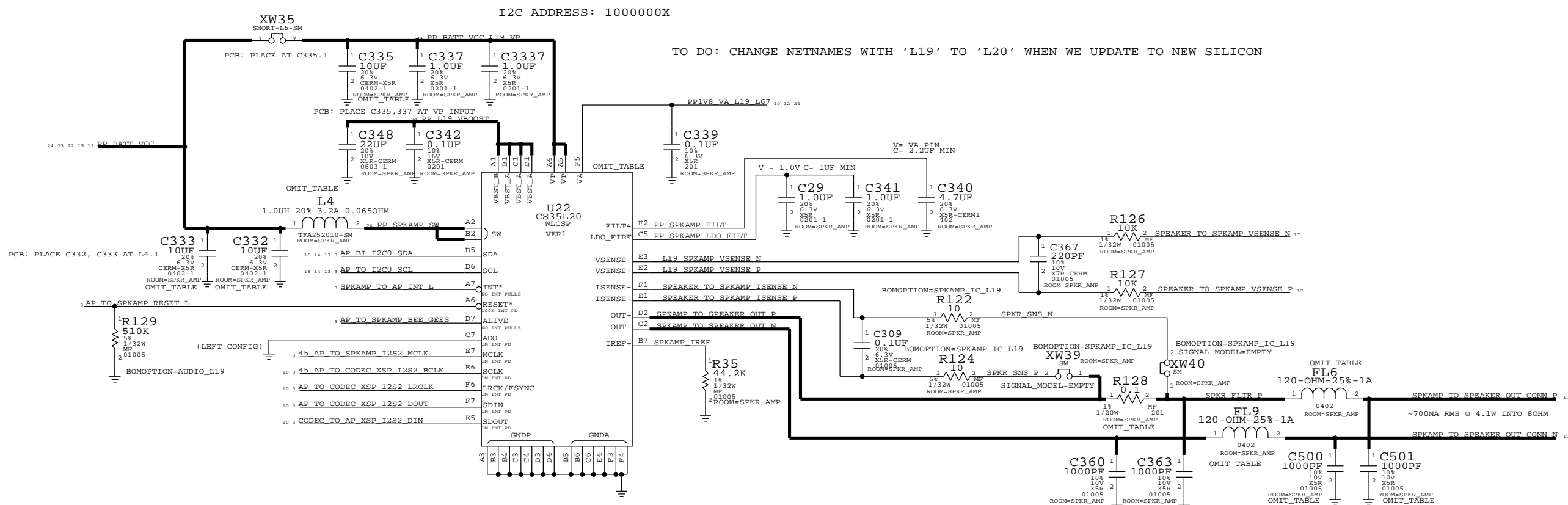
MESA BOOST



NOTE: LT3460 SPEC RECOMMENDS TO TIE PINS 2,5 TO GND

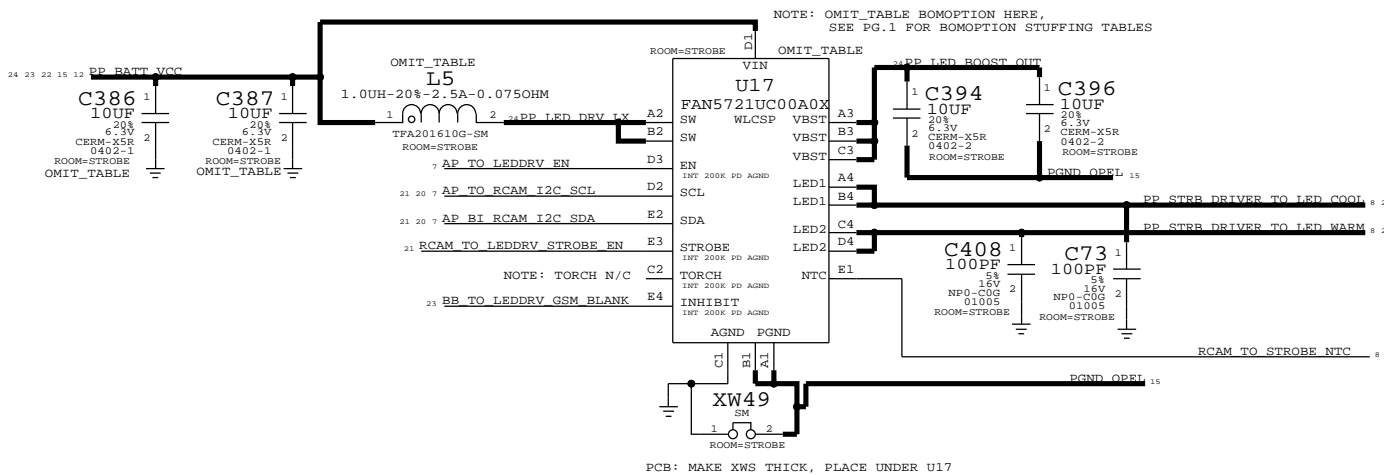
SPEAKER AMP, LED DRIVER

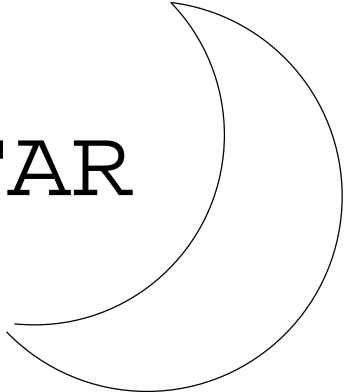
SPEAKER AMP (TO BE REPLACED WITH L20)



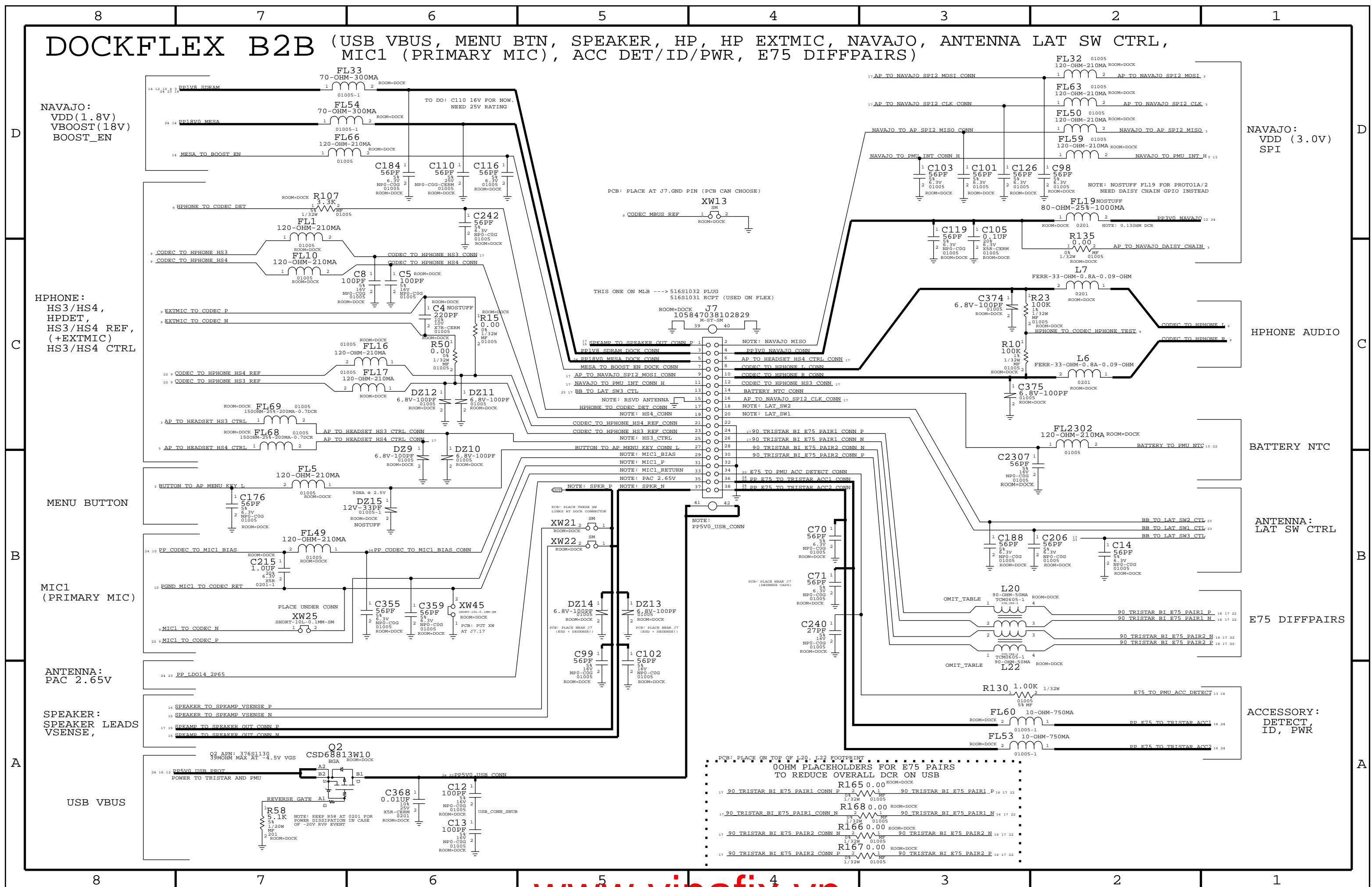
STROBE DRIVER (OPEL)

TI: APN 353S3899
FAIRCHILD: APN 353S3839





EEPROM

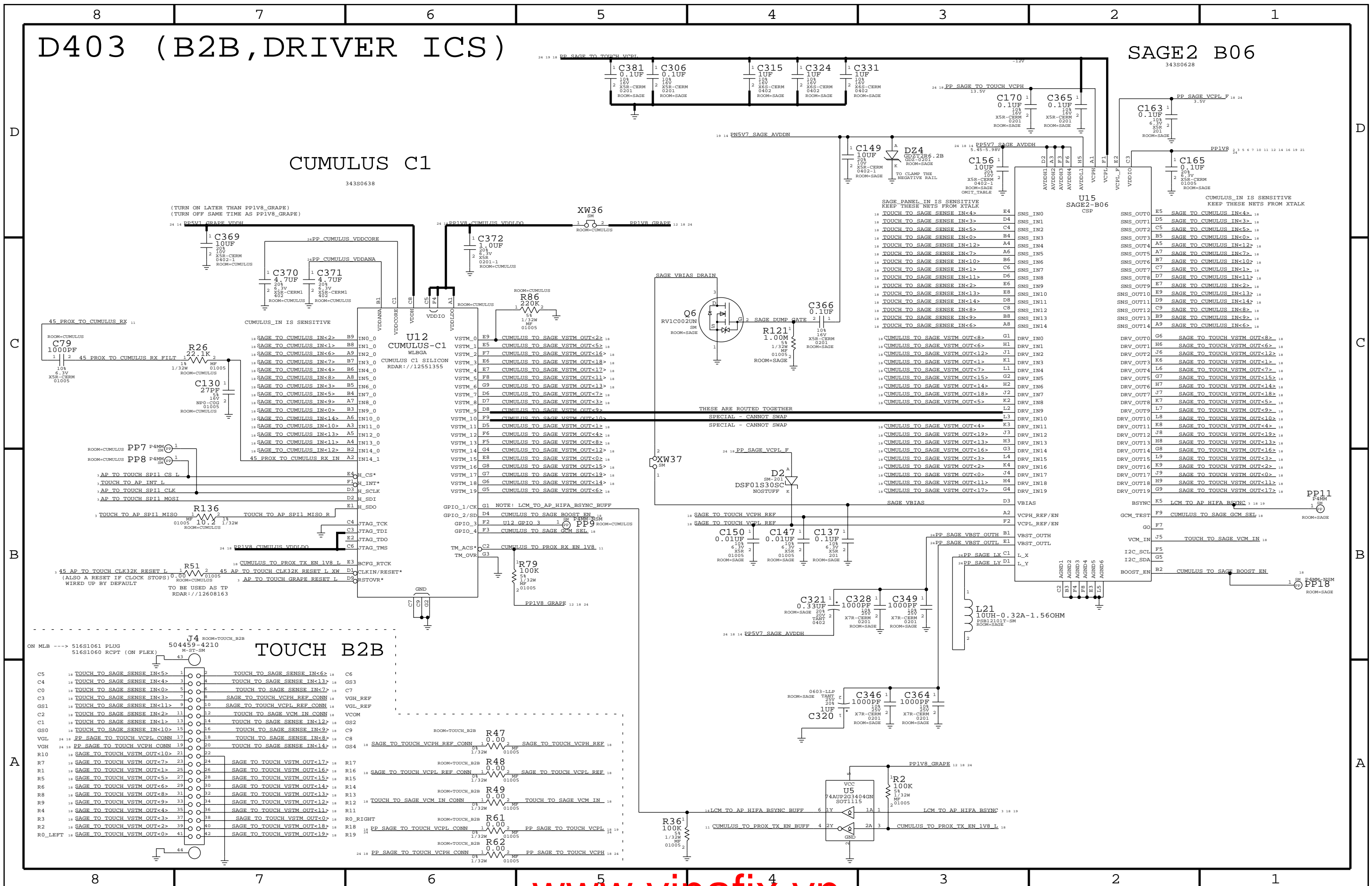


D403 (B2B, DRIVER ICS)

SAGE2 B06
34380628

CUMULUS C1

TOUCH B2B



LCM B2B

LCM:
2-LANE MIPI

LCM:
POWER
(1.8V DVDD)
(+5.7V AVDD)
(-5.7V AVDD)

LCM:
DIGITAL I/F
(PWR_EN, RESET
PIFA, BSYNC)

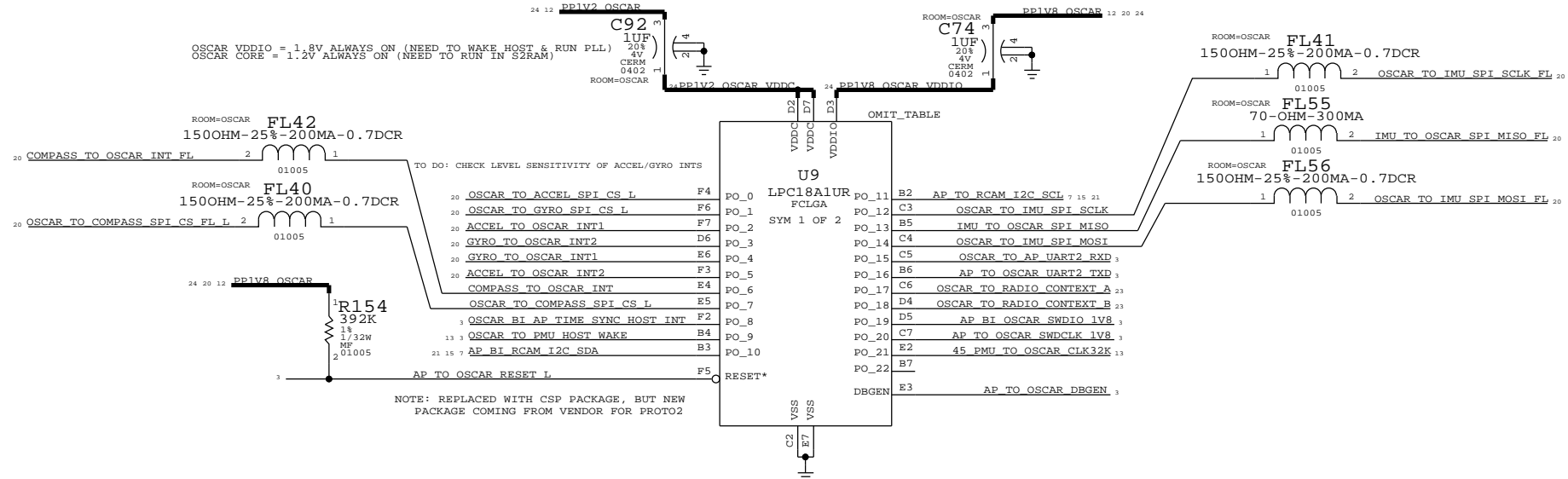
LCM:
BACKLIGHT

PCB: ALL 56PF CAPS GO AT CONN

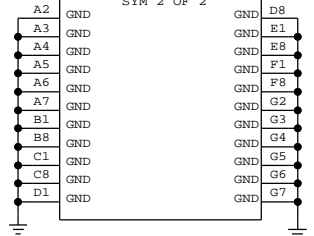
OSCAR + SENSORS

OSCAR MODULE (CONFORMAL COATED)

APN 337S4417



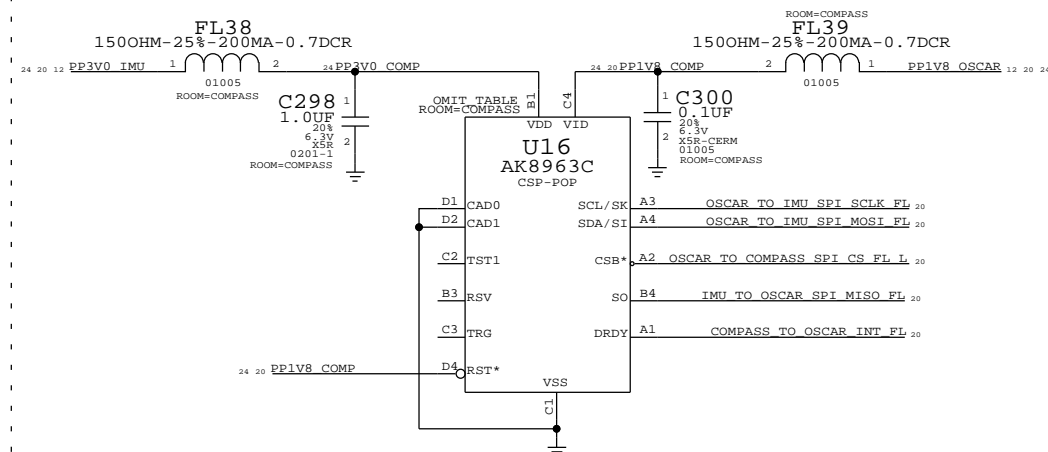
OSCAR MODULE GND BALLS (THIS SYMBOL DOES NOT EXIST ON OSCAR CSP)



THIS PART OUTSIDE OF SHIELD

COMPASS

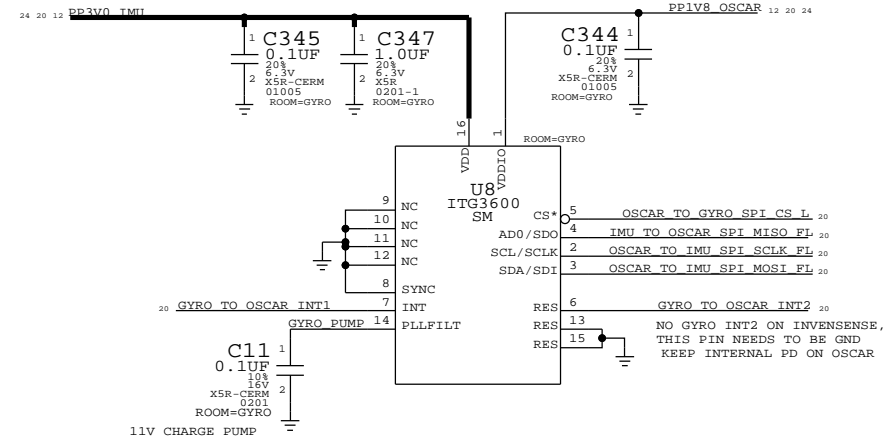
COMPASS CSP: 338S1014
COMPASS INTERPOSER (FOOTPRINT ONLY): 998-5120
COMPASS INTERPOSER MODULE: 639-4269



THESE PARTS INSIDE OF SHIELD

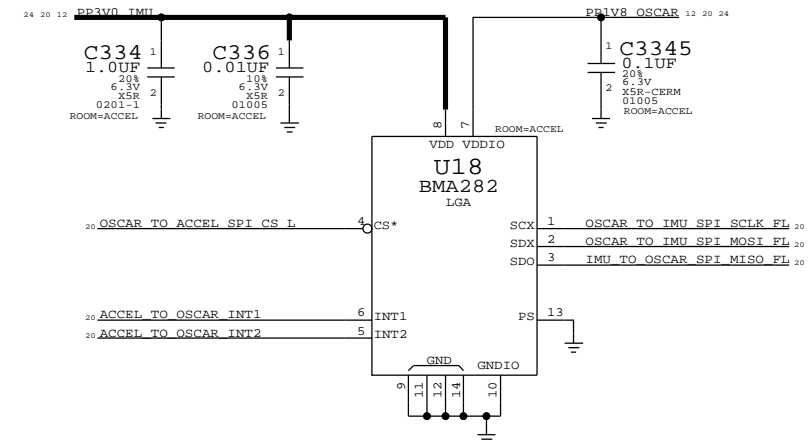
GYRO

X152: INVENSENSE ITG-3600, APN 338S1135



ACCELEROMETER

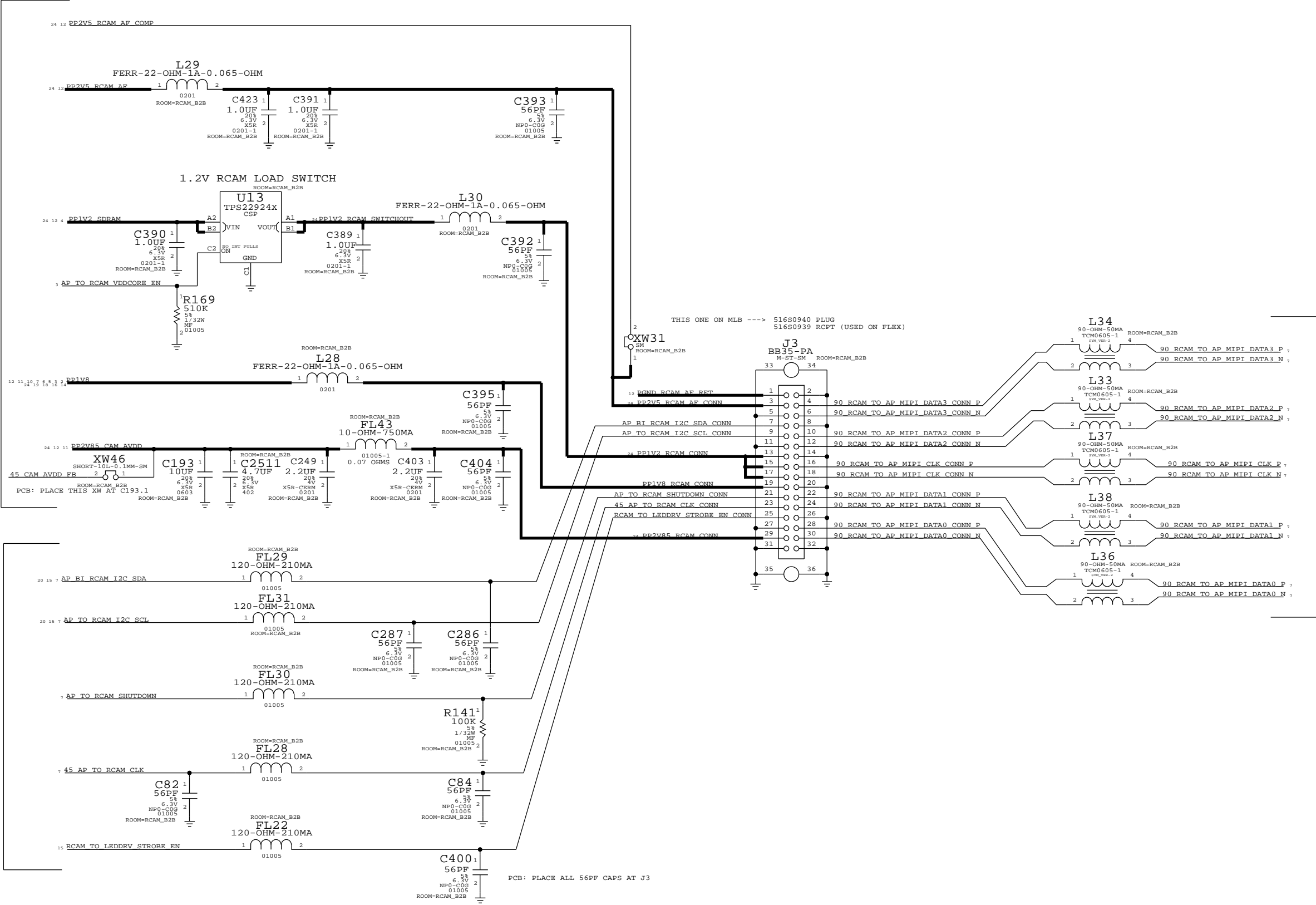
X152: BOSCH BMA282, APN 338S1163



RCAM B2B (REAR CAMERA CONNECTOR)

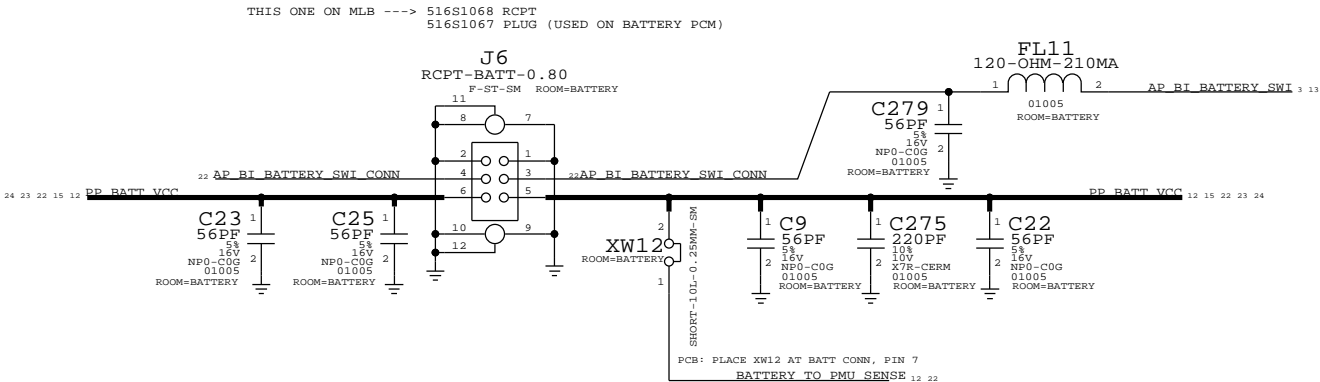
RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(2.5V AF)

RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)

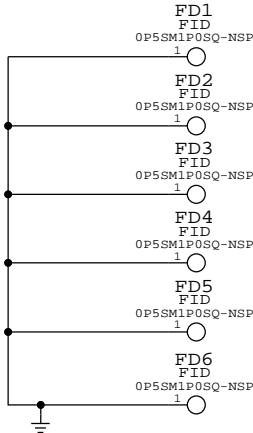


BATT CONN, TPS, STANDOFFS/SHIELDS/FIDUCIALS

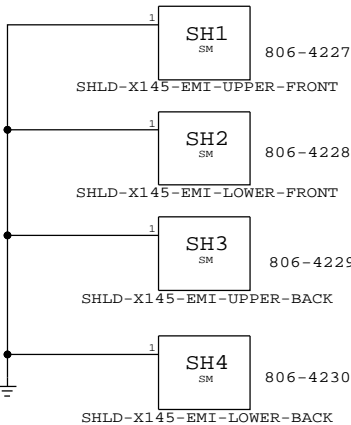
BATTERY CONN



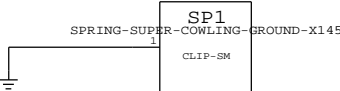
FIDUCIALS



SHIELDS

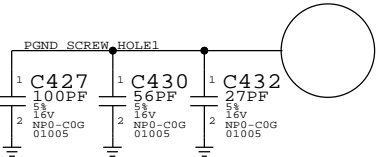


COWLING

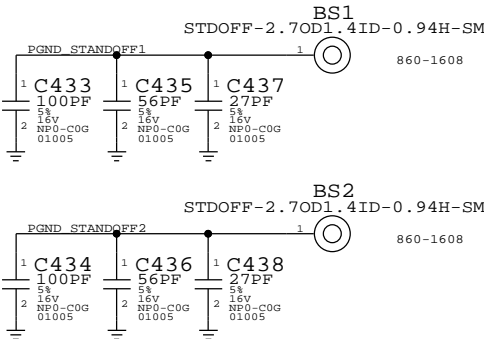


AC COUPLED SCREW HOLES + STANDOFFS
(ON NORTH END OF SINGLE_BRD, TO MITIGATE COMPASS RETURN CURRENTS)

SCREW HOLES

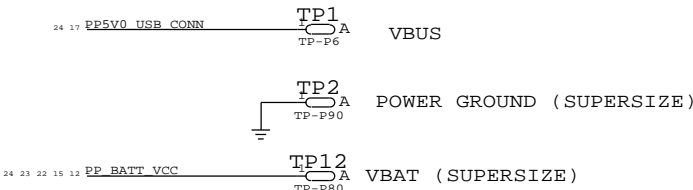


STANDOFFS

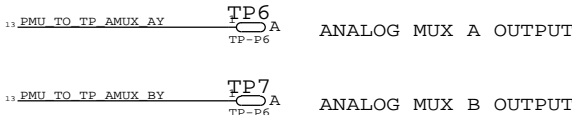


TESTPOINTS

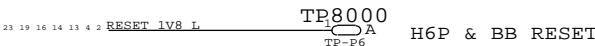
POWER TP



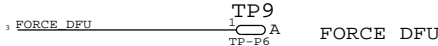
SUPER TP



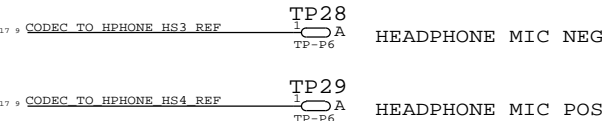
RESET



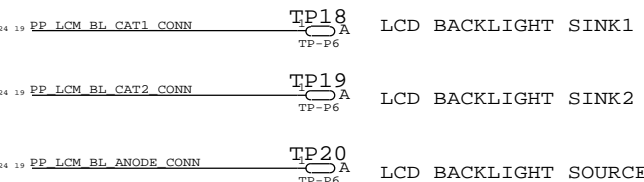
DFU



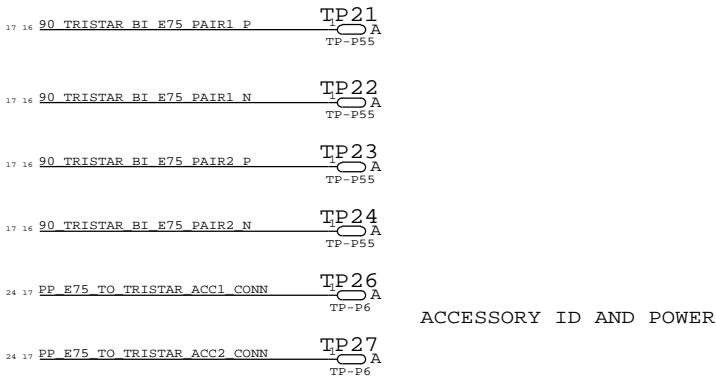
HEADPHONE MIC



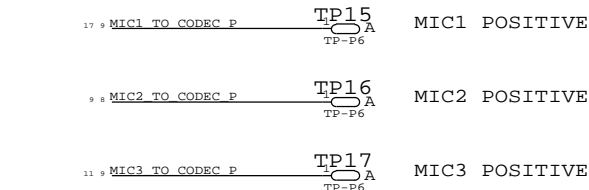
LCM BACKLIGHT



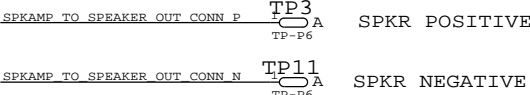
E75 - USB/UART/ID/POWER



MIC AUDIO



DRIVE MIC WRT NEAREST GROUND TEST POINT

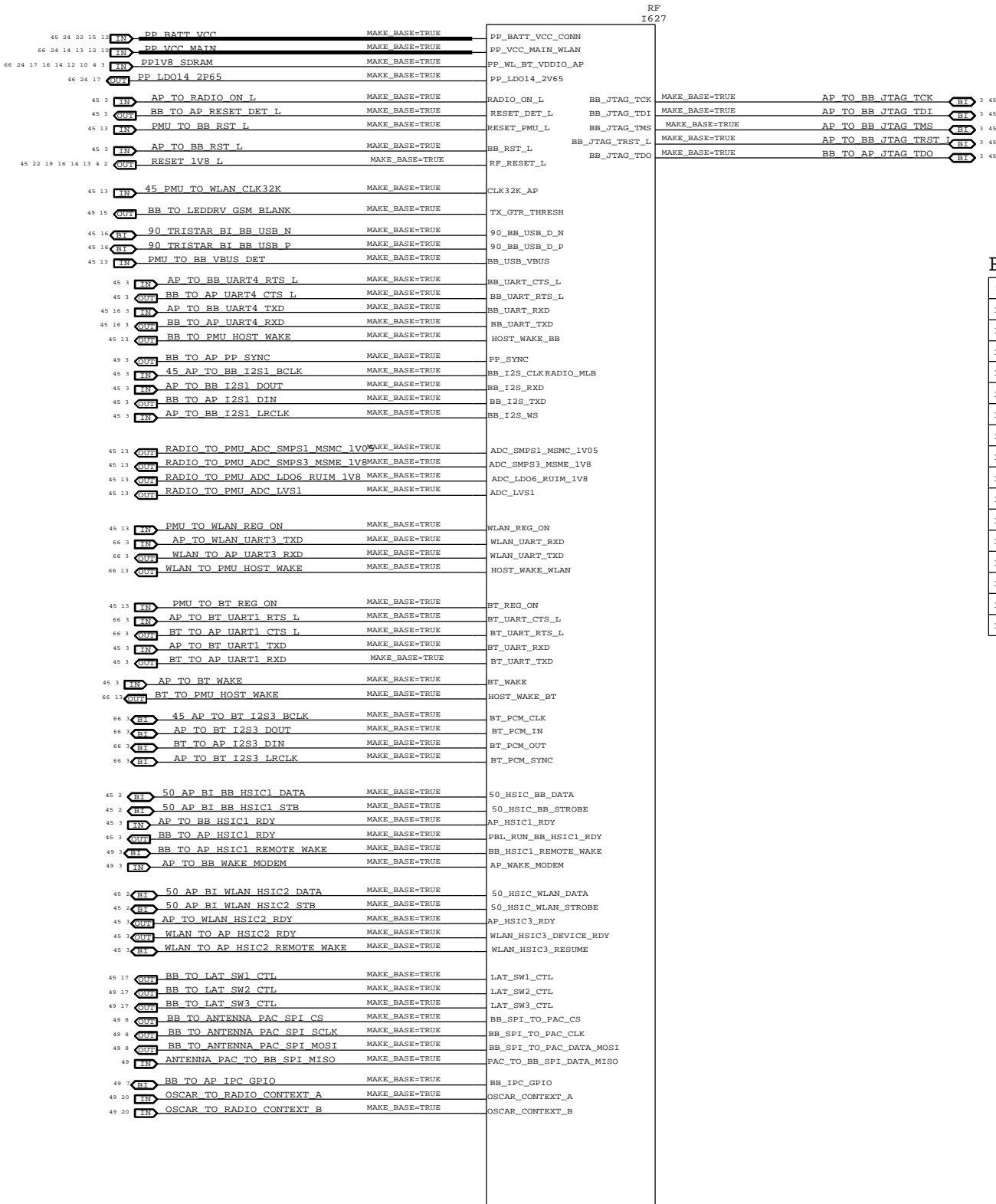


ADDED PER
RDAR://12460740

[illegible]

RADIO_MLB HIERARCHICAL SYMBOL

AP/RADIO INTERFACE



BOARD_ID BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
118S0621	1	1.00M 1% 01005	R25_RF	Y	N51_CFG_A
118S0732	1	50K 1% 01005	R26_RF	Y	N51_CFG_A
117S0159	1	470K 5% 01005	R25_RF	Y	N51_CFG_B
118S0626	1	100K 1% 01005	R26_RF	Y	N51_CFG_B
118S0626	1	100K 1% 01005	R25_RF	Y	N53_CFG_A
118S0726	1	162K 1% 01005	R26_RF	Y	N53_CFG_A
118S0626	1	100K 1% 01005	R25_RF	Y	N53_CFG_B
118S0623	1	267K 1% 01005	R26_RF	Y	N53_CFG_B
118S0659	1	255K 1% 01005	R25_RF	Y	N48_CFG_A
118S0626	1	100K 1% 01005	R26_RF	Y	N48_CFG_A
118S0689	1	147K 1% 01005	R26_RF	Y	N48_CFG_B
118S0626	1	100K 1% 01005	R26_RF	Y	N48_CFG_B
118S0626	1	100K 1% 01005	R25_RF	Y	N49_CFG_A
118S0650	1	499K 1% 01005	R26_RF	Y	N49_CFG_A
118S0732	1	50K 1% 01005	R25_RF	Y	N49_CFG_B
118S0621	1	1.00M 1% 01005	R26_RF	Y	N49_CFG_B

D

C

B

A

PDF	PAGE	CSA	PAGE	CONTENTS
2	2			AP INTERFACE & DEBUG CONNECTORS
3	3			PMU (1 OF 2)
4	4			PMU (2 OF 2)
5	5			BASEBAND (1 OF 2)
6	6			BASEBAND (2 OF 2)
7	7			RF TRANSCEIVER (1 OF 2)
8	8			RF TRANSCEIVER (2 OF 2)
9	9			RX MATCHING
10	10			TX INTERSTAGE FILTERS
11	11			BAND 1/34/39/38/40 TX
12	12			BAND 2/3 PAD
13	13			BAND 7/20 PAD
14	14			BAND 5/8 PAD
15	15			2G PA
16	16			PA DCDC CONVERTER
17	17			PRIMARY ASM
18	18			RX DIVERSITY
19	19			GPS
20	20			ANTENNA FEEDS
21	21			SWITCH LOGIC
22	22			BLANK
23	23			WIFI/BT

SCH : 951-2770
BOM : 639-3973
BOARD : 820-3382

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
951-2445	1	X152_RADIO_MLB	SCH	Y	
825-2029	1	EEE FOR 939-0308	EEEE_???	Y	NA

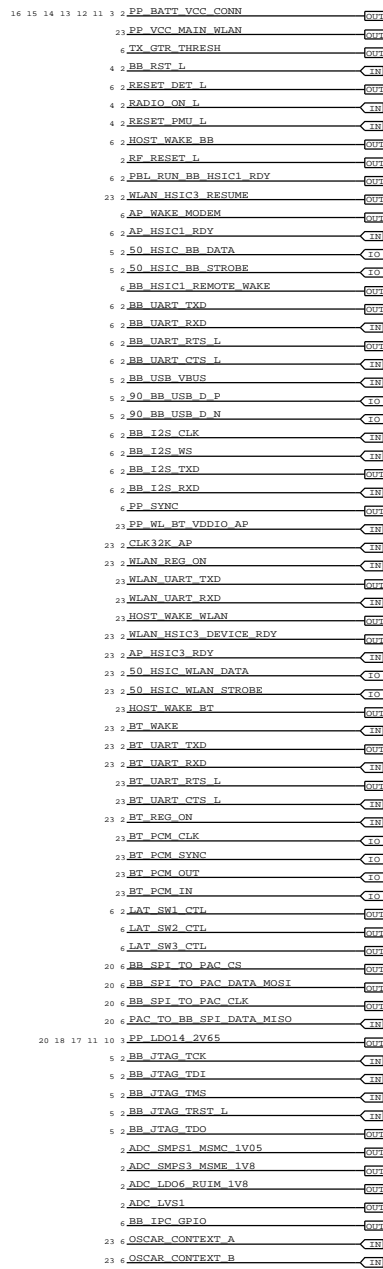
BOARD_ID BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
118S0621	1	1.00M 1% 01005	R25_RF	Y	N51_CFG_A
118S0732	1	50K 1% 01005	R26_RF	Y	N51_CFG_A
117S0159	1	470K 5% 01005	R25_RF	Y	N51_CFG_B
118S0626	1	100K 1% 01005	R26_RF	Y	N51_CFG_B
118S0626	1	100K 1% 01005	R25_RF	Y	N53_CFG_A
118S0726	1	162K 1% 01005	R26_RF	Y	N53_CFG_A
118S0626	1	100K 1% 01005	R25_RF	Y	N53_CFG_B
118S0623	1	267K 1% 01005	R26_RF	Y	N53_CFG_B
118S0659	1	255K 1% 01005	R25_RF	Y	N48_CFG_A
118S0626	1	100K 1% 01005	R26_RF	Y	N48_CFG_A
118S0689	1	147K 1% 01005	R26_RF	Y	N48_CFG_B
118S0626	1	100K 1% 01005	R26_RF	Y	N48_CFG_B
118S0626	1	100K 1% 01005	R25_RF	Y	N49_CFG_A
118S0650	1	499K 1% 01005	R26_RF	Y	N49_CFG_A
118S0732	1	50K 1% 01005	R25_RF	Y	N49_CFG_B
118S0621	1	1.00M 1% 01005	R26_RF	Y	N49_CFG_B

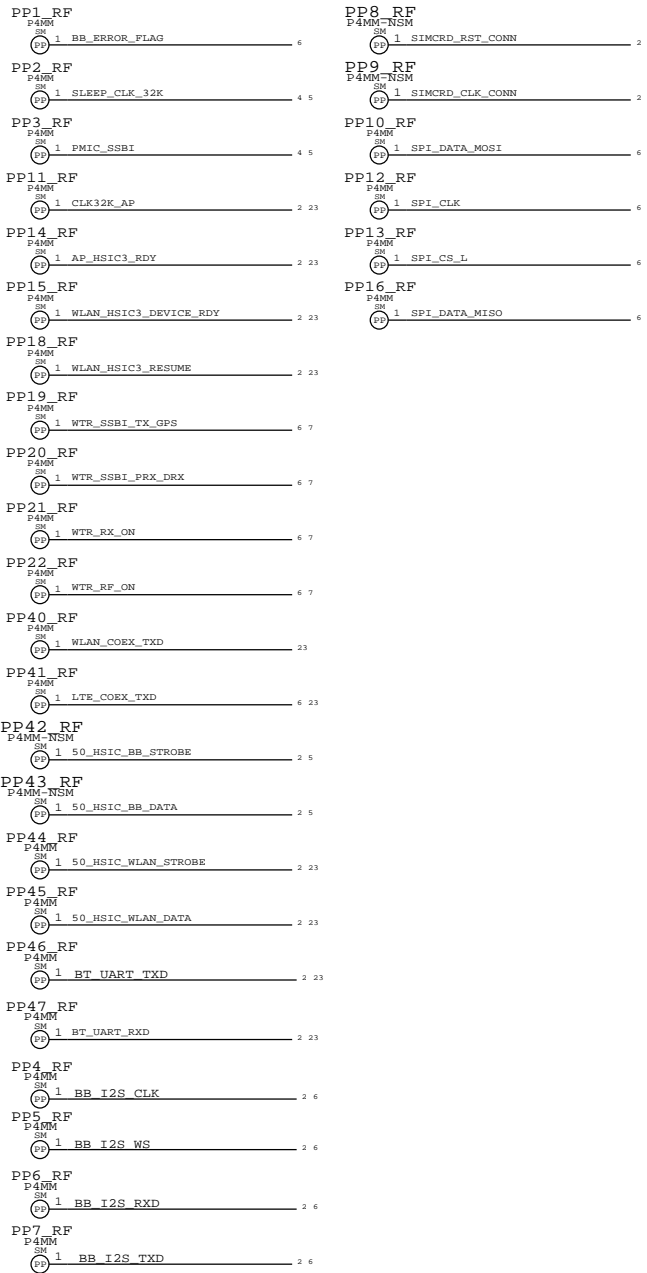
AP INTERFACE & DEBUG CONNECTORS

AP CONNECTIONS

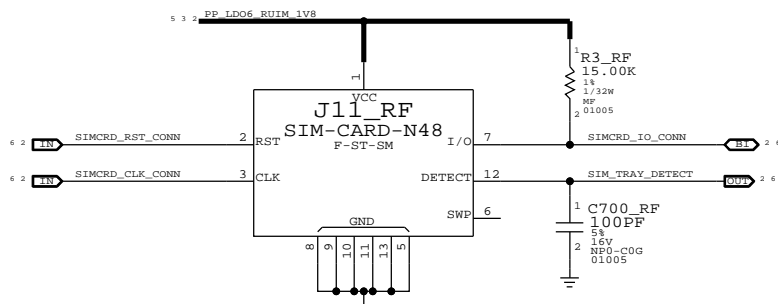
IN = FROM AP
OUT = TO AP



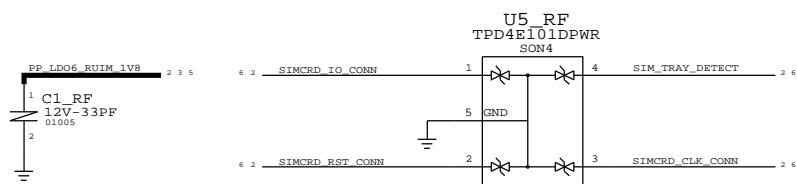
PROBE POINTS



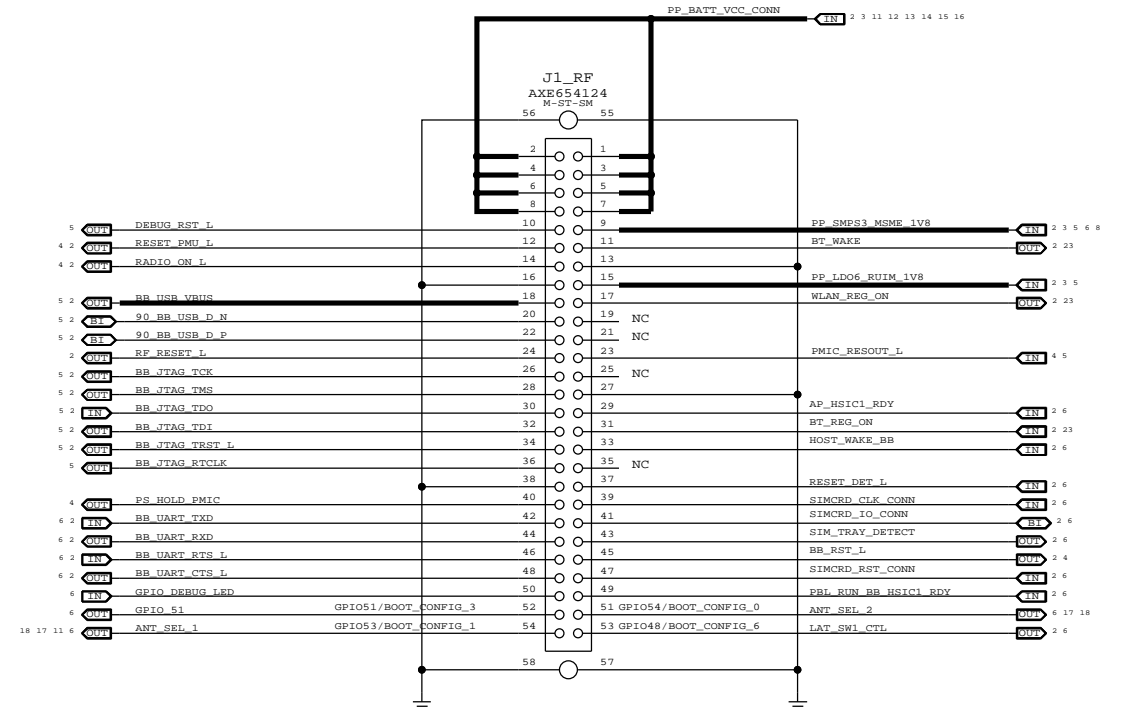
SIM CARD CONNECTOR



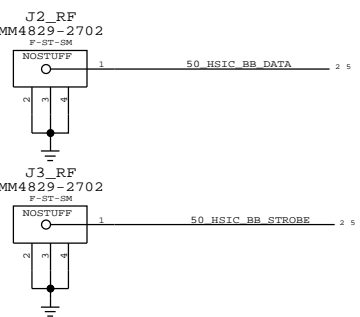
SIM CARD ESD PROTECTION



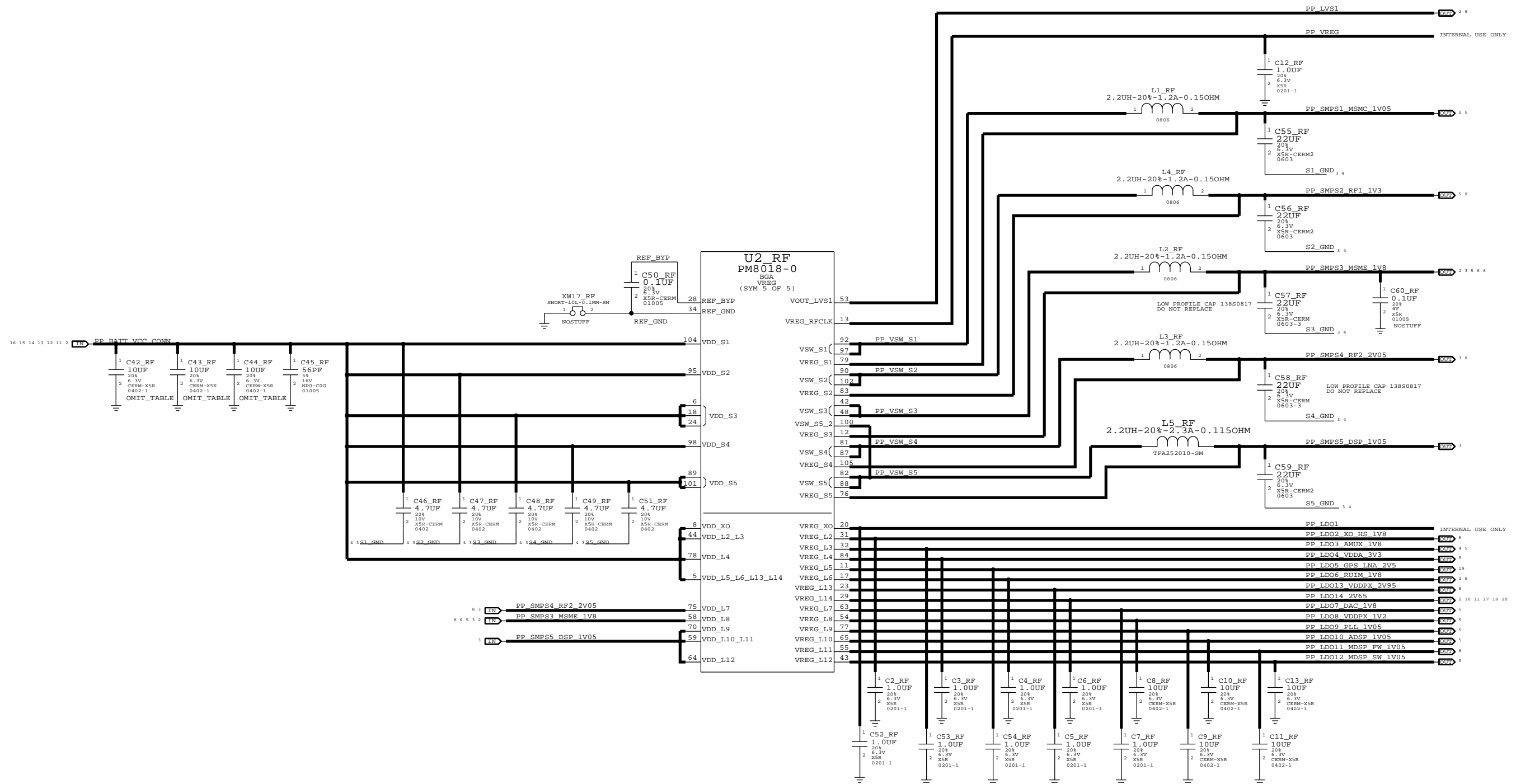
DEBUG CONNECTOR



BOOT OPTIONS	BOOT_CONFIG SW REGISTER VALUE	GPIO/BOOT_CONFIG CONFIGURATION							
		6	5	4	3	2	1	0	
BOOT_DEFAULT_OPTION	0x00	X	0	0	0	0	0	0	X
BOOT_NAND_OPTION	0x01	X	1	0	0	0	0	0	1
BOOT_HSIC_OPTION	0x02	X	1	0	0	0	0	1	0
BOOT_USB_OPTION	0x03	X	1	0	0	0	0	1	1
ENABLE_SAHARA_PROTOCOL	0x08	X	1	0	0	1	0	X	X



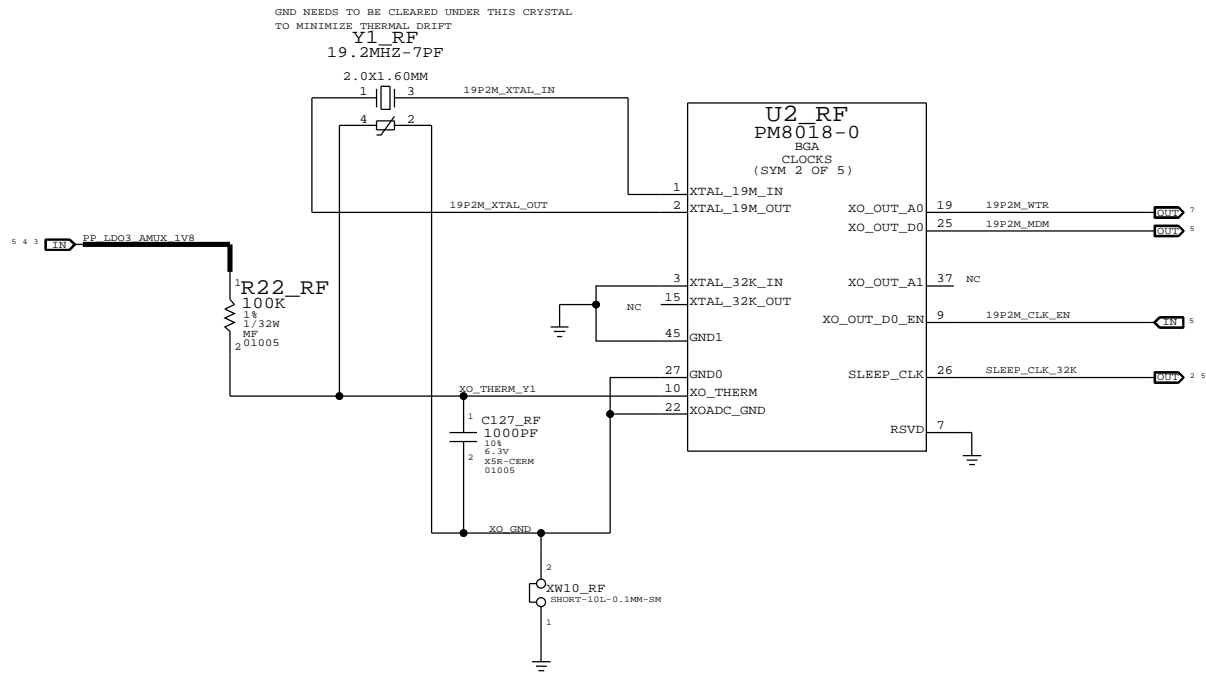
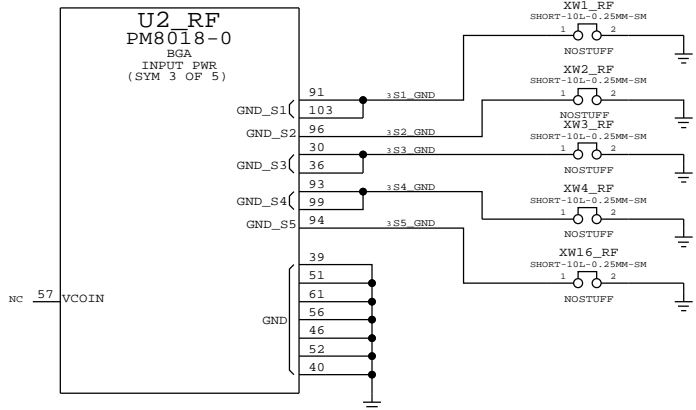
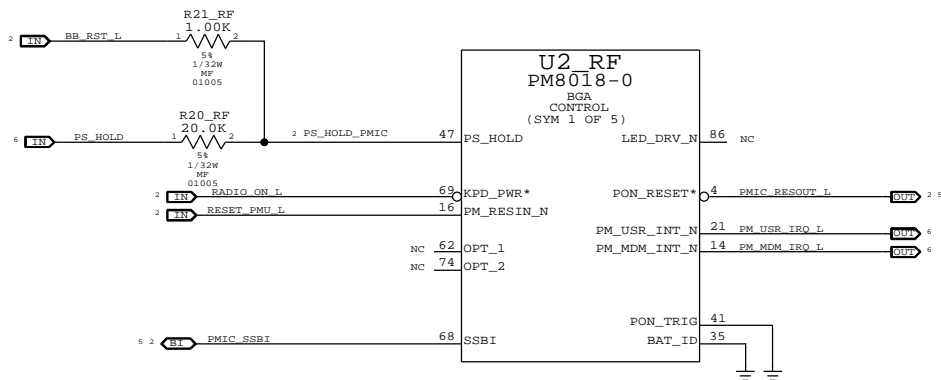
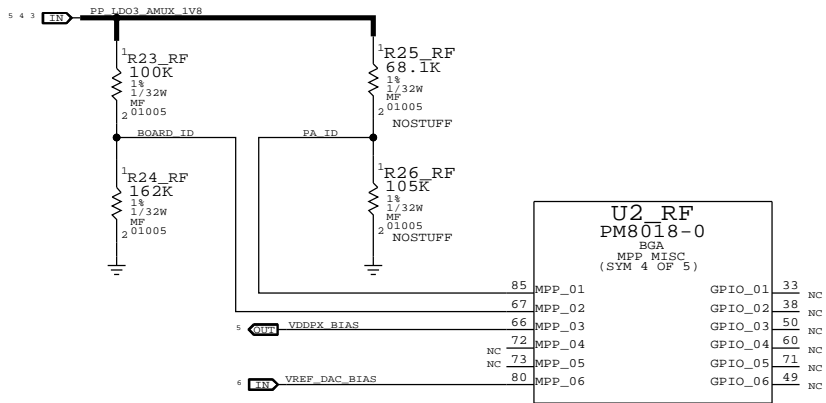
PMU (1 OF 2)



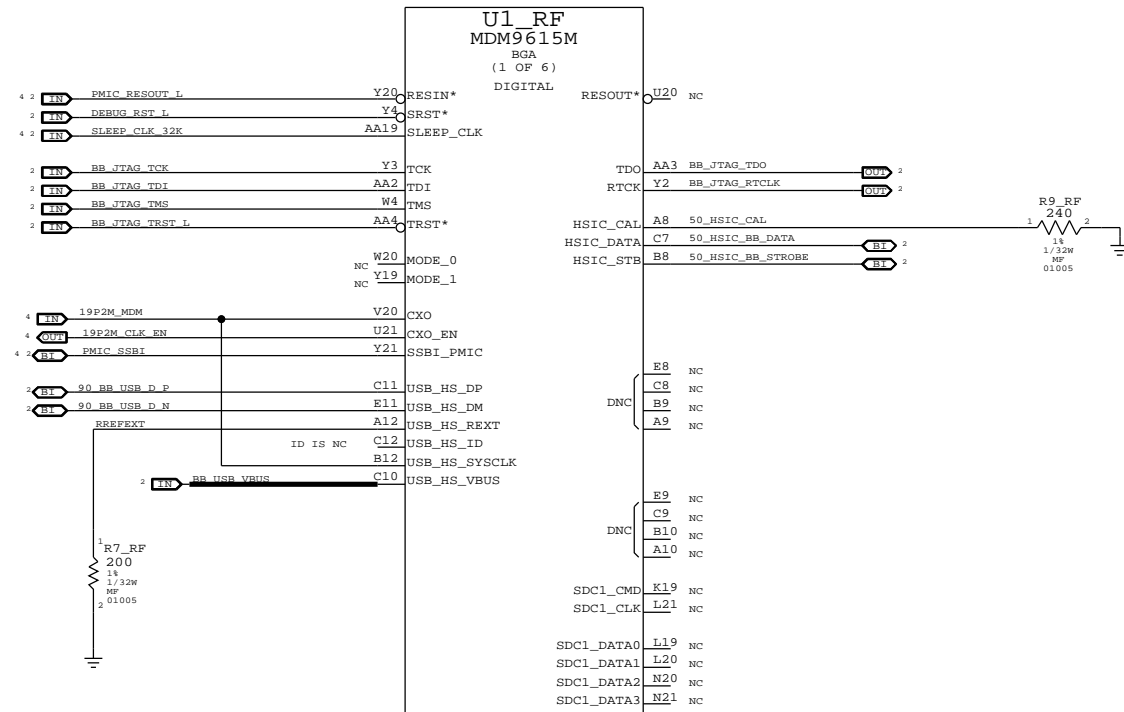
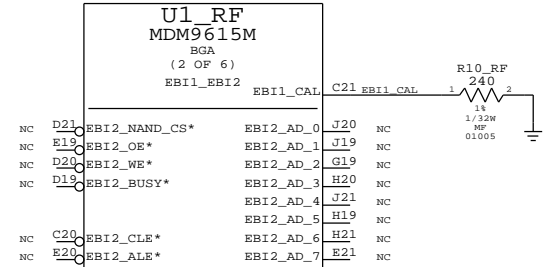
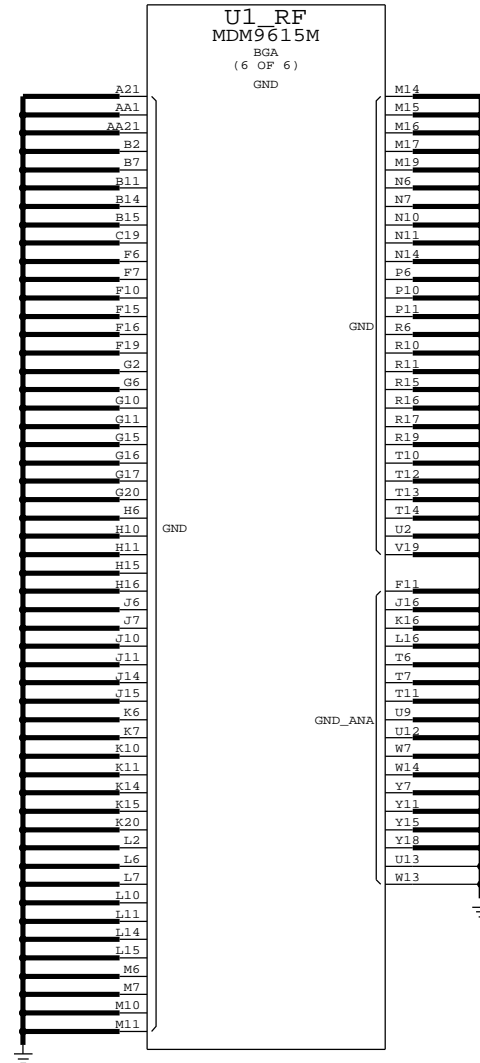
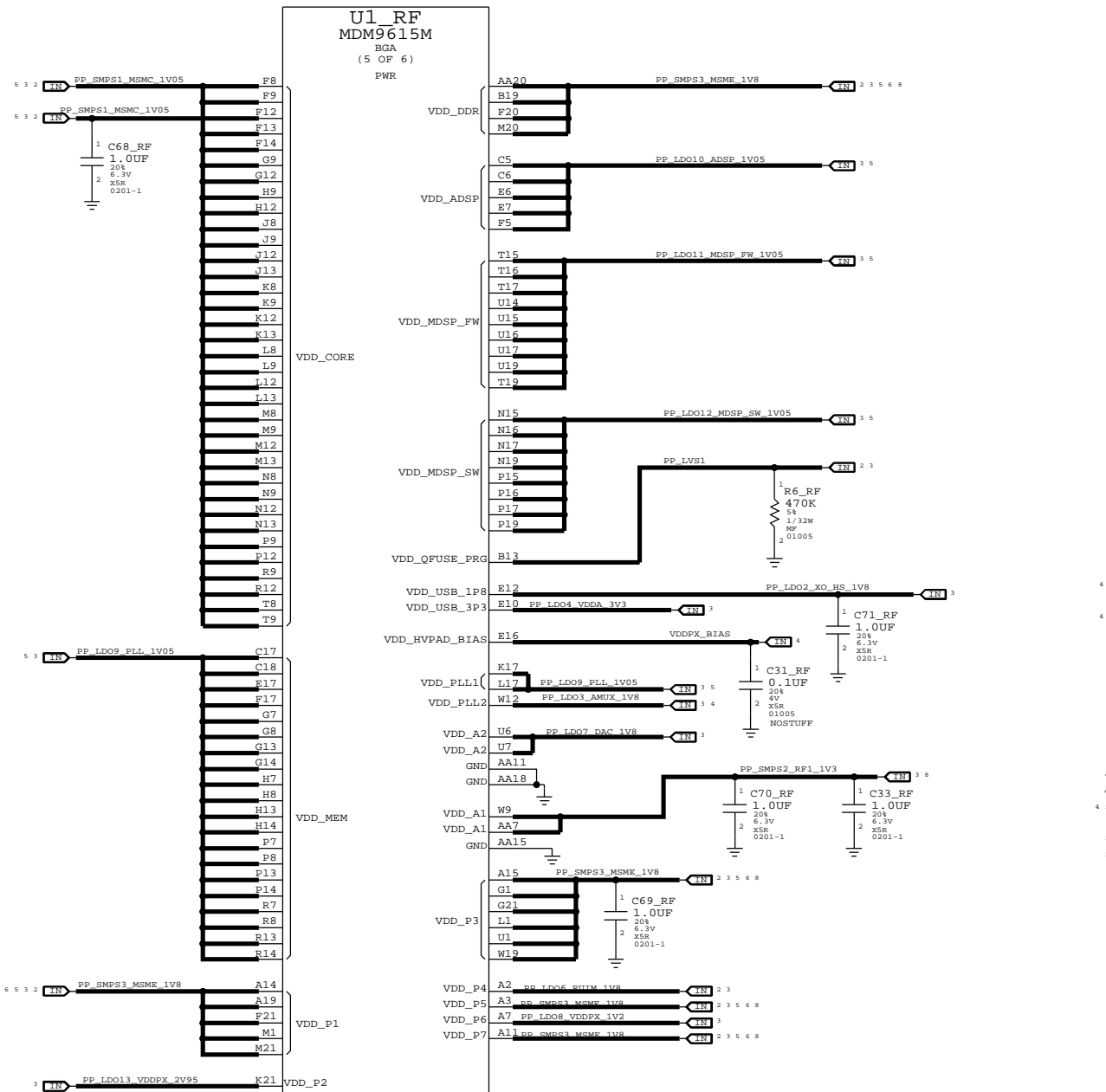
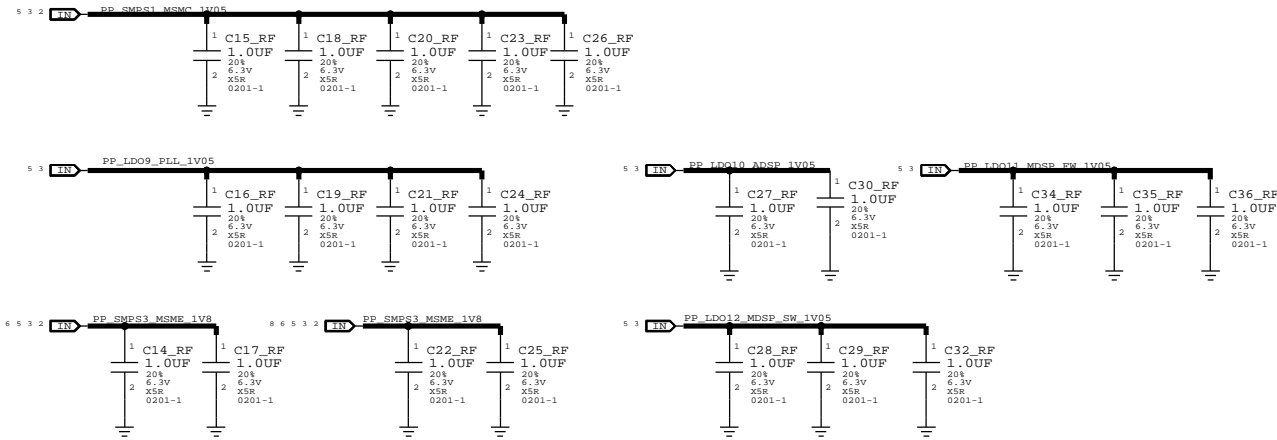
PMU (2 OF 2)

PA_ID	CONFIG
1.1V	CONFIG A
1.3V	CONFIG B
1.5V	CONFIG C
1.7V	CONFIG D

BOARD_ID	REVISION
0.7V	PROTO1
0.9V	PROTO2
1.1V	EVT1
1.3V	EVT2
1.5V	DVT
1.7V	PVT

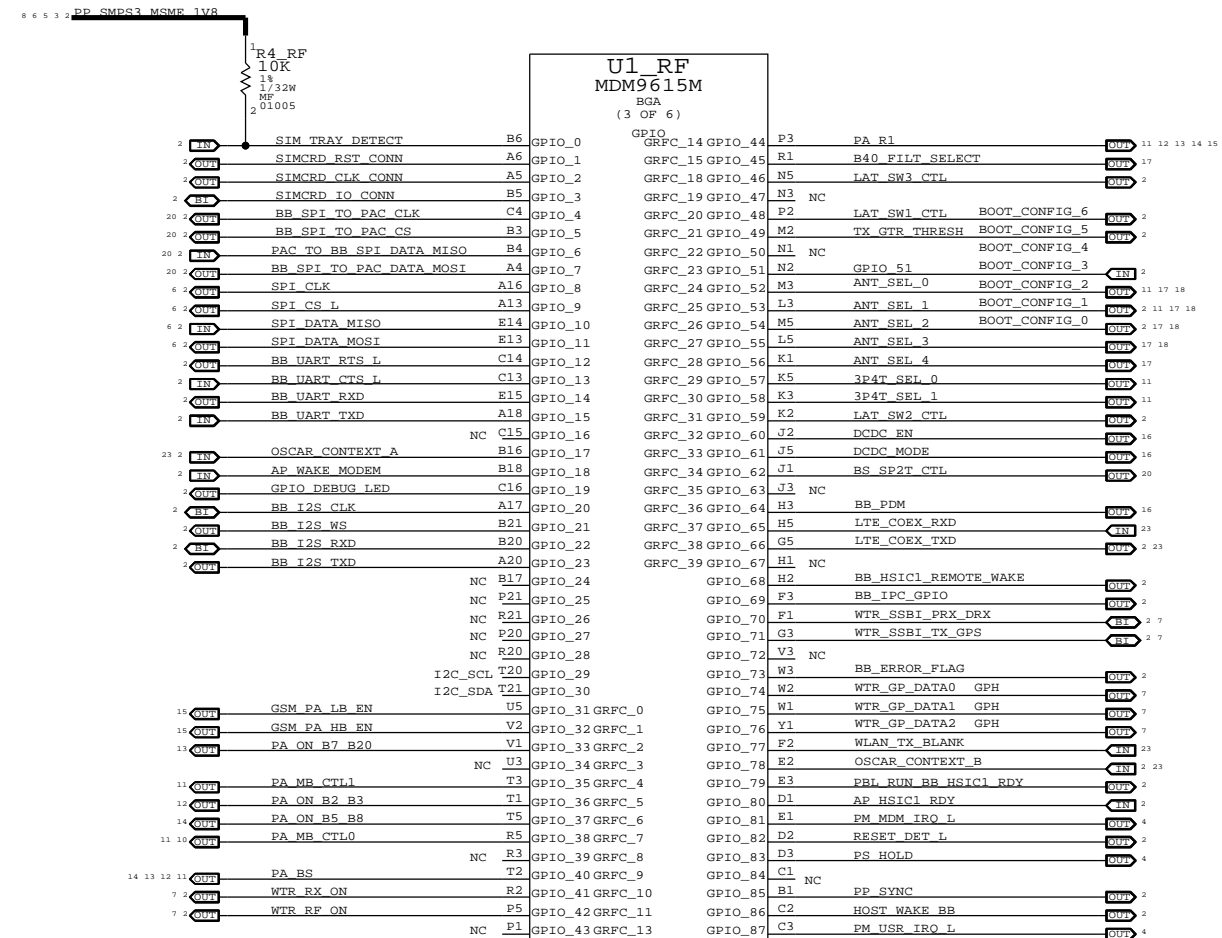
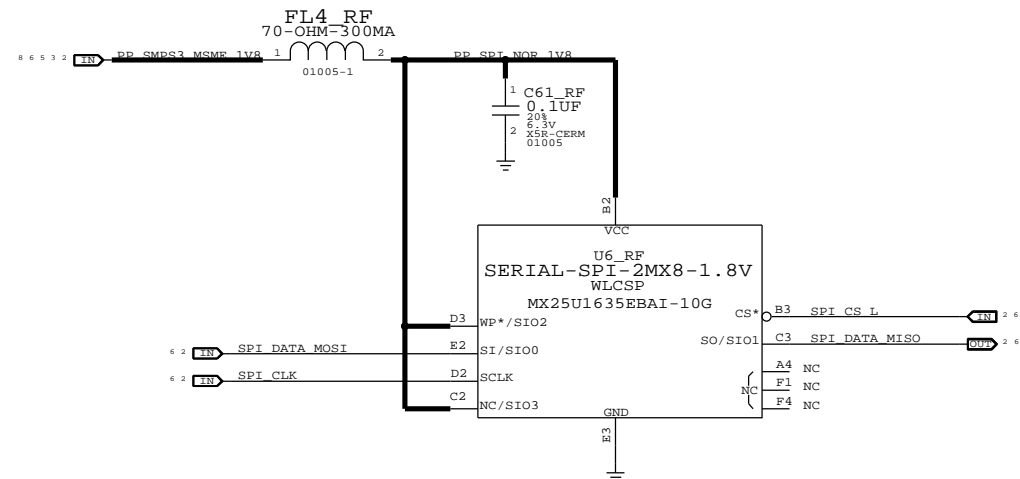
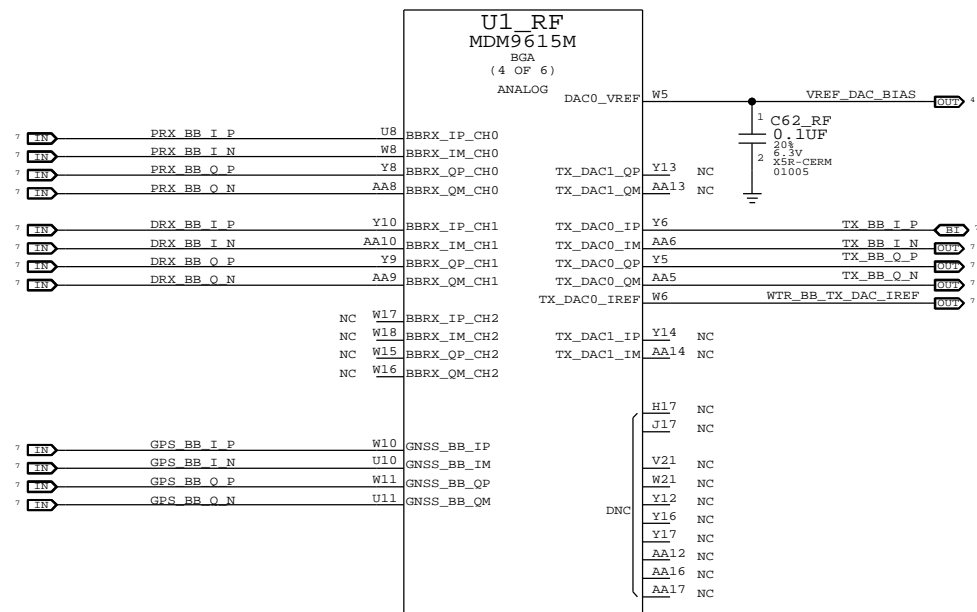


BASEBAND (1 OF 2)



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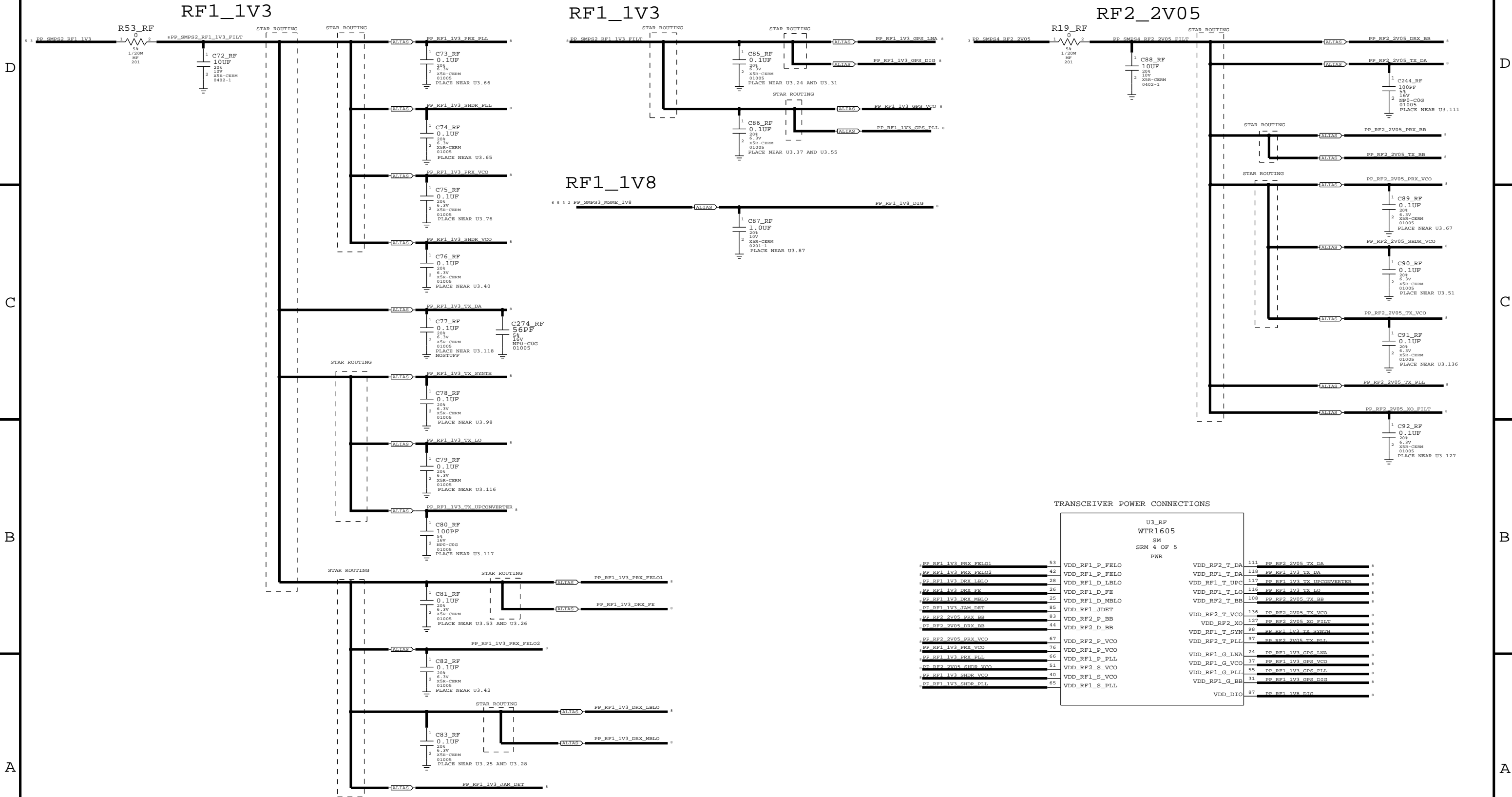
BASEBAND (2 OF 2)



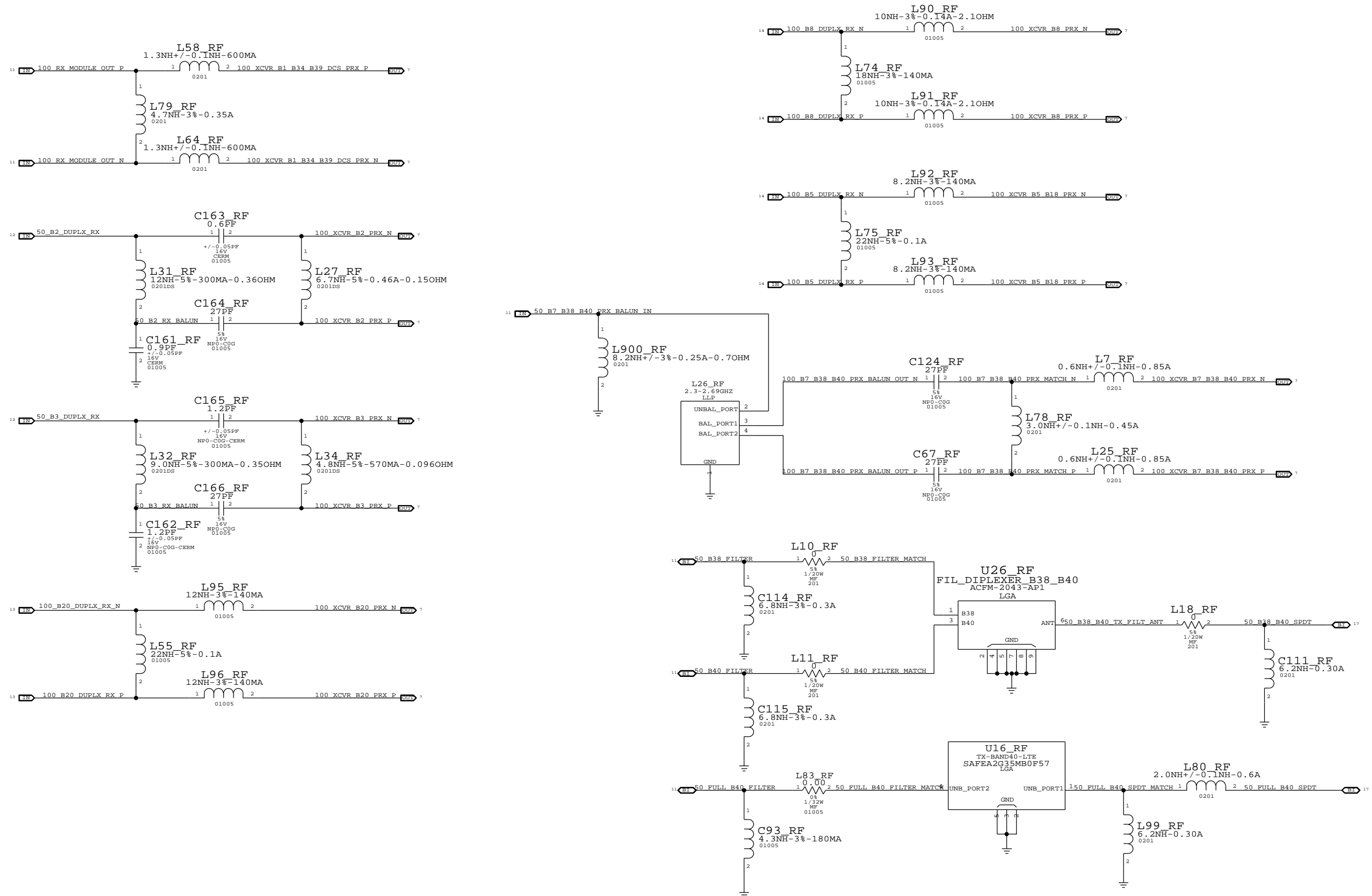
A



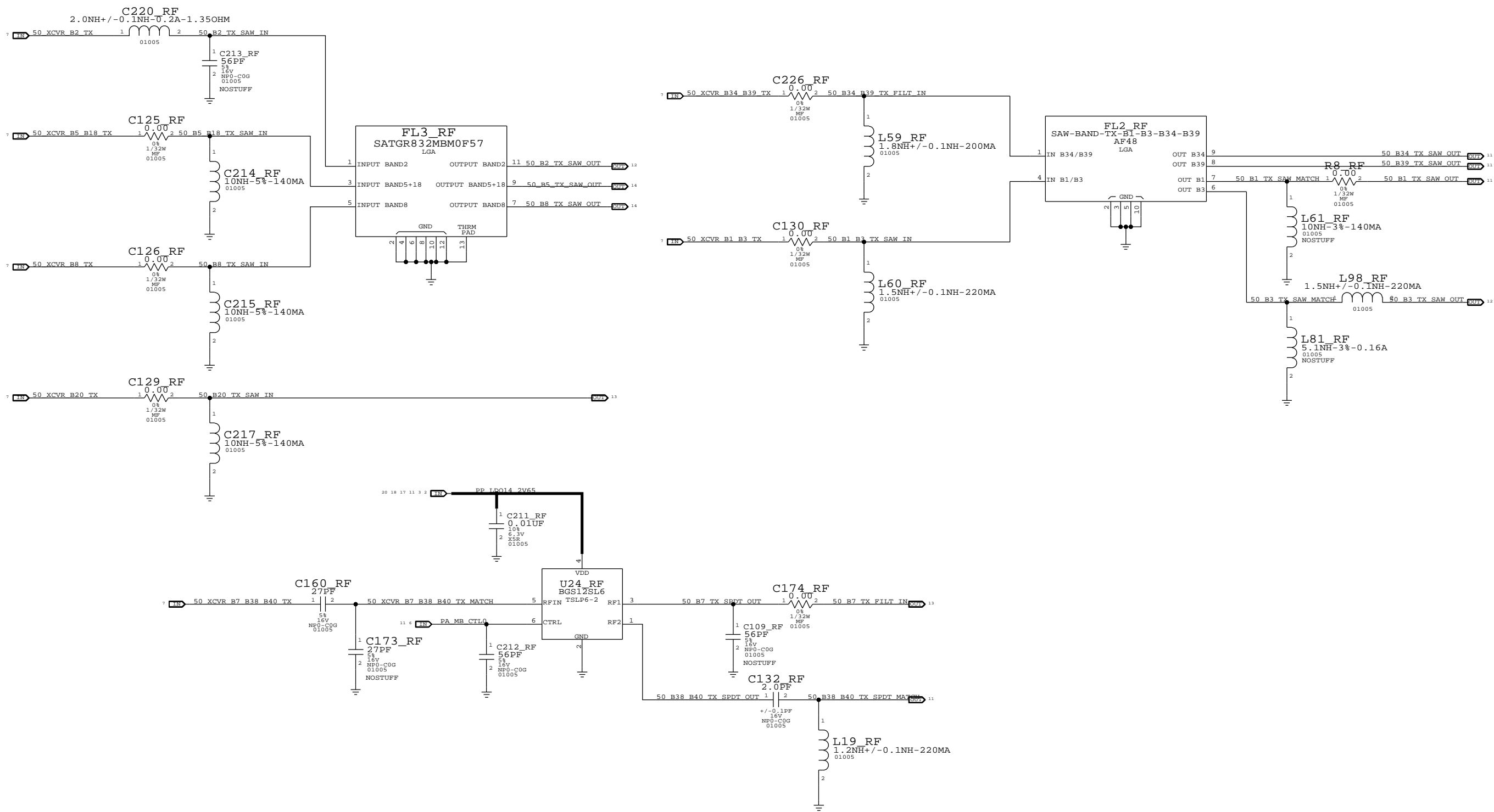
RF TRANSCEIVER (2 OF 2)



RX MATCHING



TX INTERSTAGE FILTERS

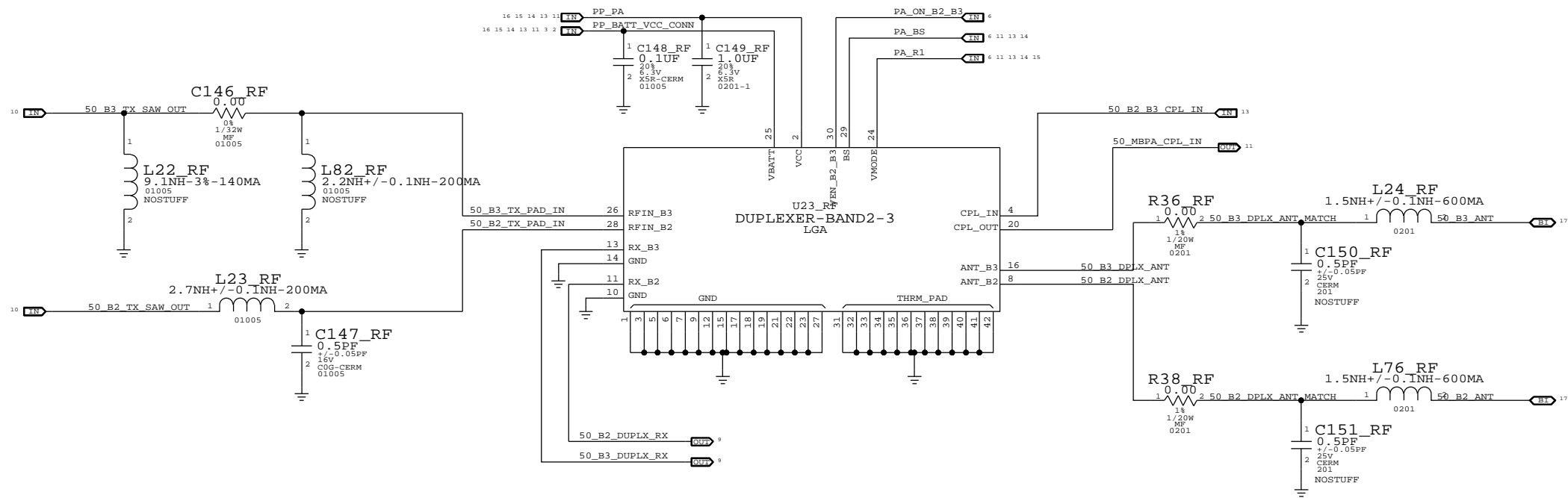


A



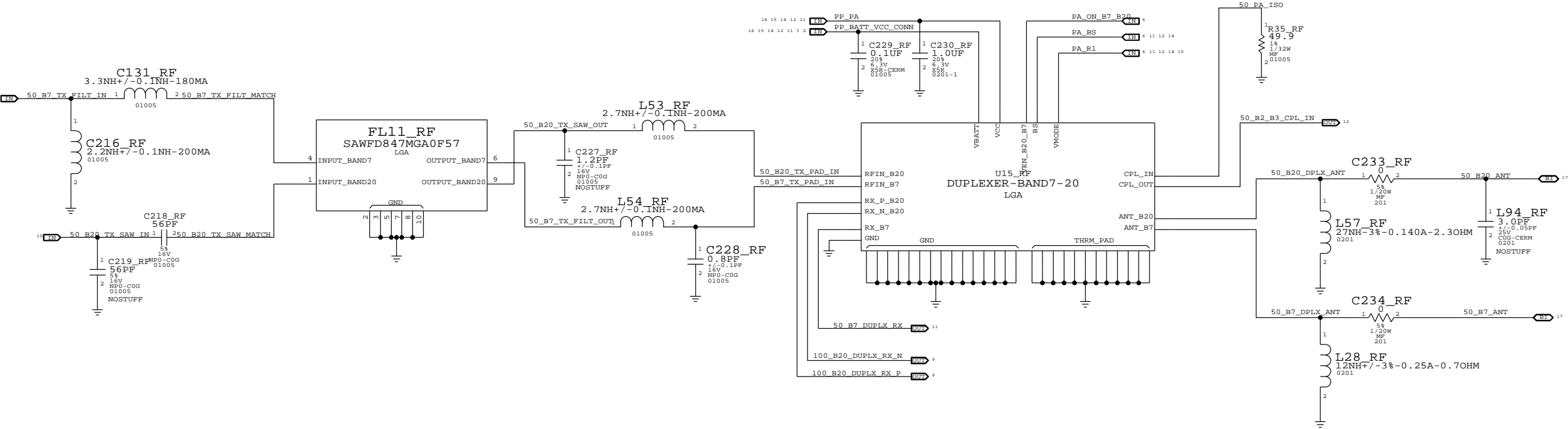
www.vinafix.vn

BAND 2 / 3 PAD



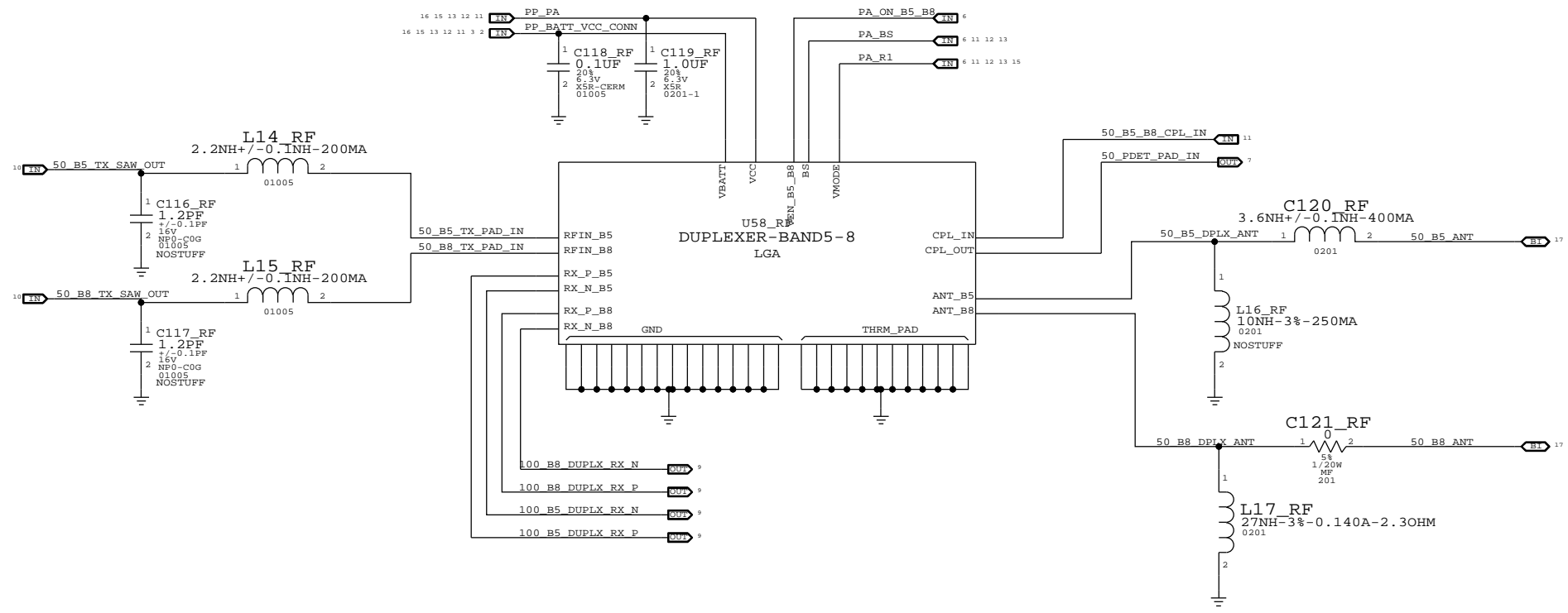
BAND	PA	POWER	MODE	PA_BS	PA_ON_B2_B3	PA_R1
=====	=====	=====	=====	=====	=====	=====
OFF		X		X	0	X
B3		HPM		0	1	0
B3		LPM		0	1	1
B2		HPM		1	1	0
B2		LPM		1	1	1

BAND 20/7 PAD



BAND	PA	POWER	MODE	PA_BS	PA_ON_B20_B7	PA_R1
=====	=====	=====	=====	=====	=====	=====
OFF		X		X	0	X
B20		HPM		0	1	0
B20		LPM		0	1	1
B7		HPM		1	1	0
B7		LPM		1	1	1

BAND 5 / 8 PAD



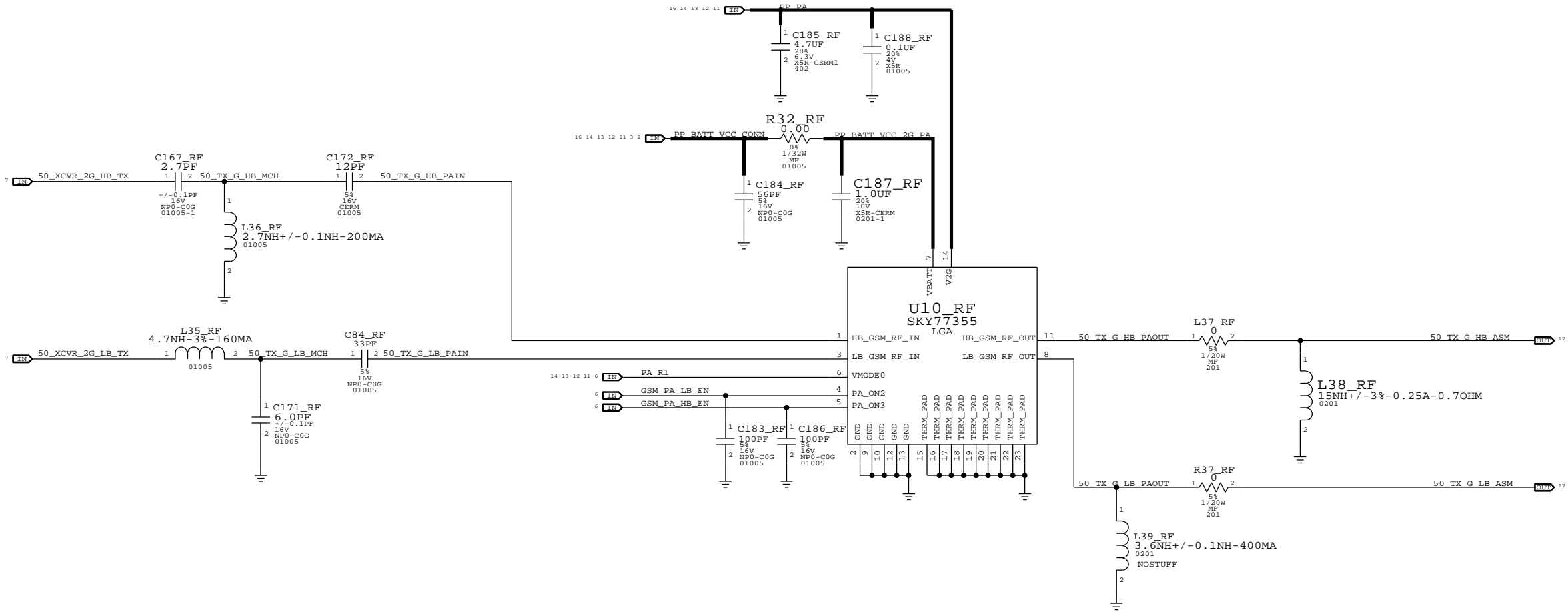
BAND	PA	POWER	MODE	PA_BS	PA_ON_B5_B8	PA_R1
=====	=====	=====	=====	=====	=====	=====
OFF		X		X	0	X
B5		HPM		0	1	0
B5		LPM		0	1	1
B8		HPM		1	1	0
B8		LPM		1	1	1

2G PA

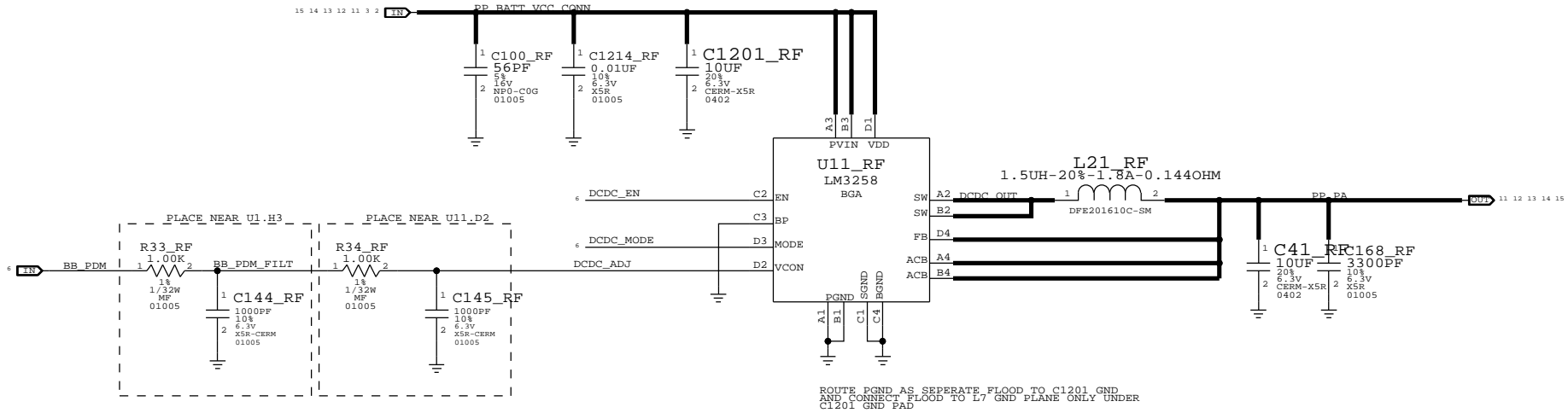
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

2G PA GAIN MODES

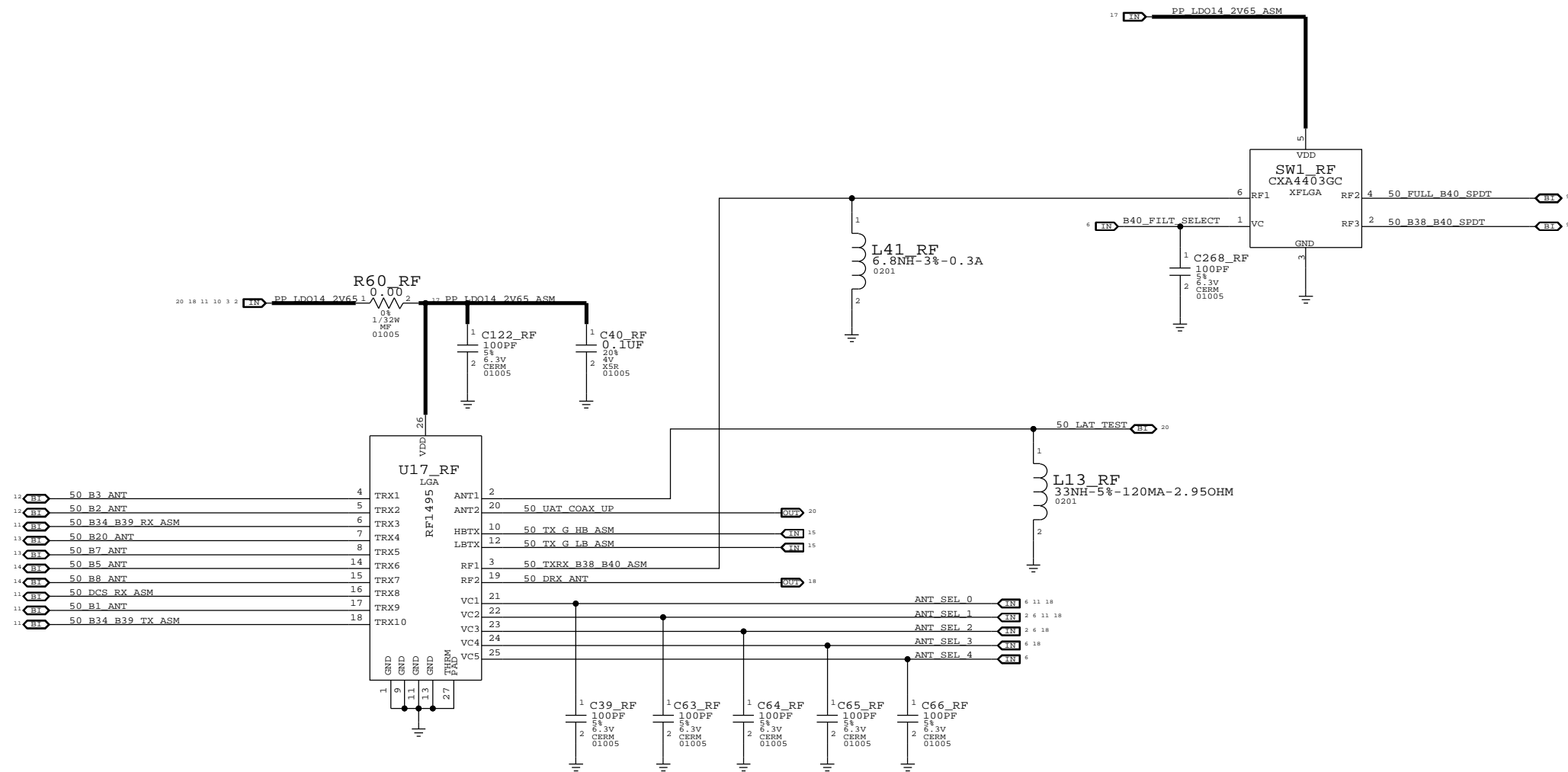
BAND	MODE	GAIN MODE	PA_R1	PCL RANGE
LOW BAND	GSM	ULTRA LOW	HIGH	16 TO 19
LOW BAND	GSM	LOW	HIGH	14 TO 15
LOW BAND	GSM	MEDIUM	LOW	7 TO 13
LOW BAND	GSM	HIGH	LOW	5 TO 6
HIGH BAND	GSM	ULTRA LOW	HIGH	10 TO 15
HIGH BAND	GSM	LOW	HIGH	7 TO 9
HIGH BAND	GSM	HIGH	LOW	0 TO 6
LOW BAND	EDGE	LOW	HIGH	15 TO 19
LOW BAND	EDGE	MEDIUM	LOW	10 TO 14
LOW BAND	EDGE	HIGH	LOW	8 TO 9
HIGH BAND	EDGE	LOW	HIGH	9 TO 15
HIGH BAND	EDGE	HIGH	LOW	2 TO 8



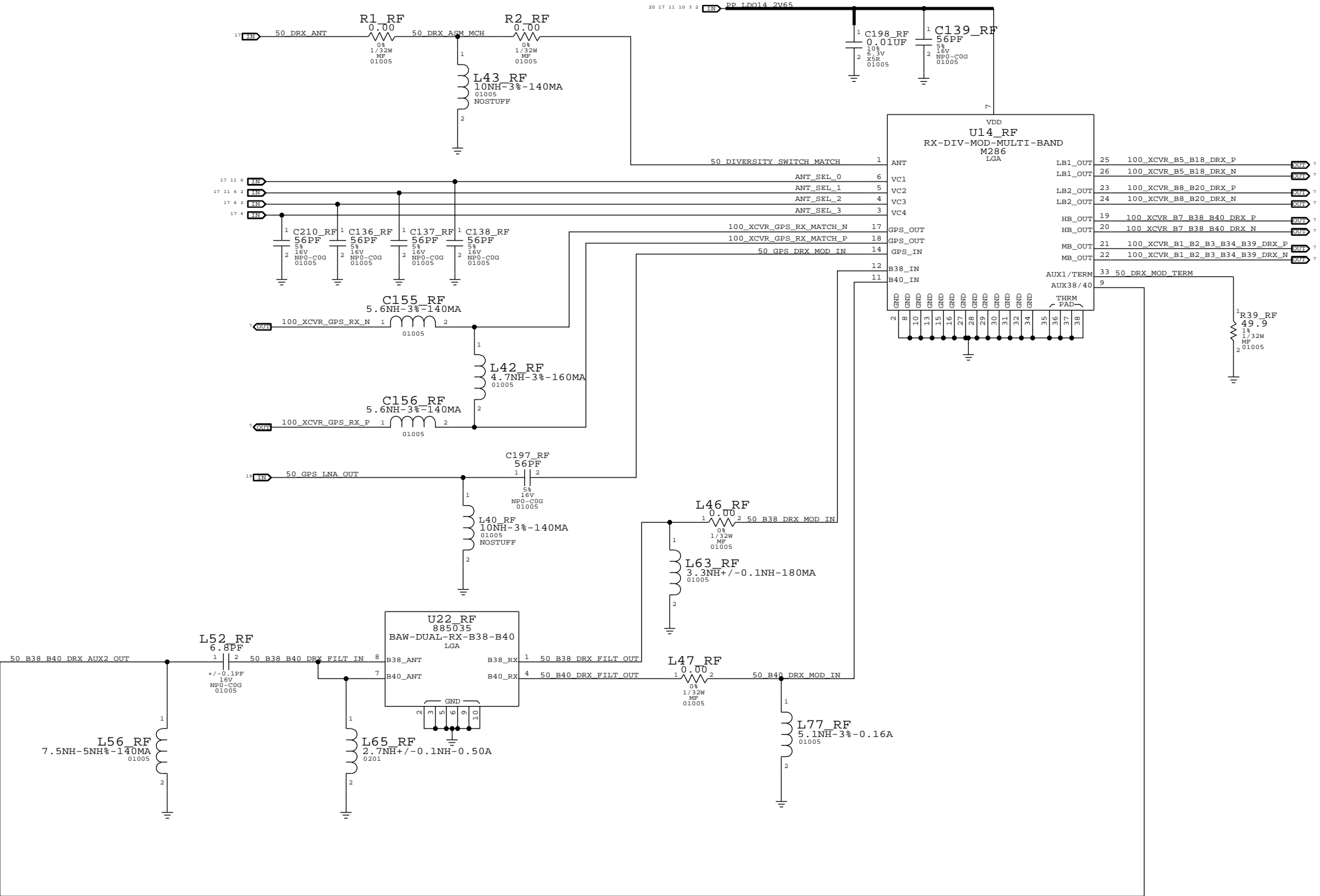
PA DC/DC CONVERTER



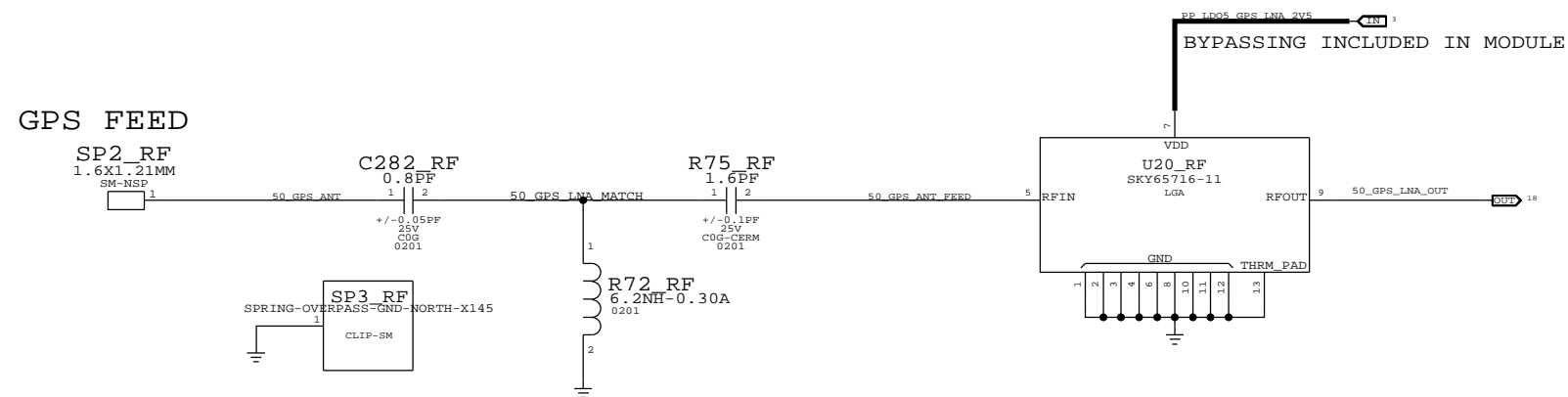
PRIMARY ASM



RX DIVERSITY

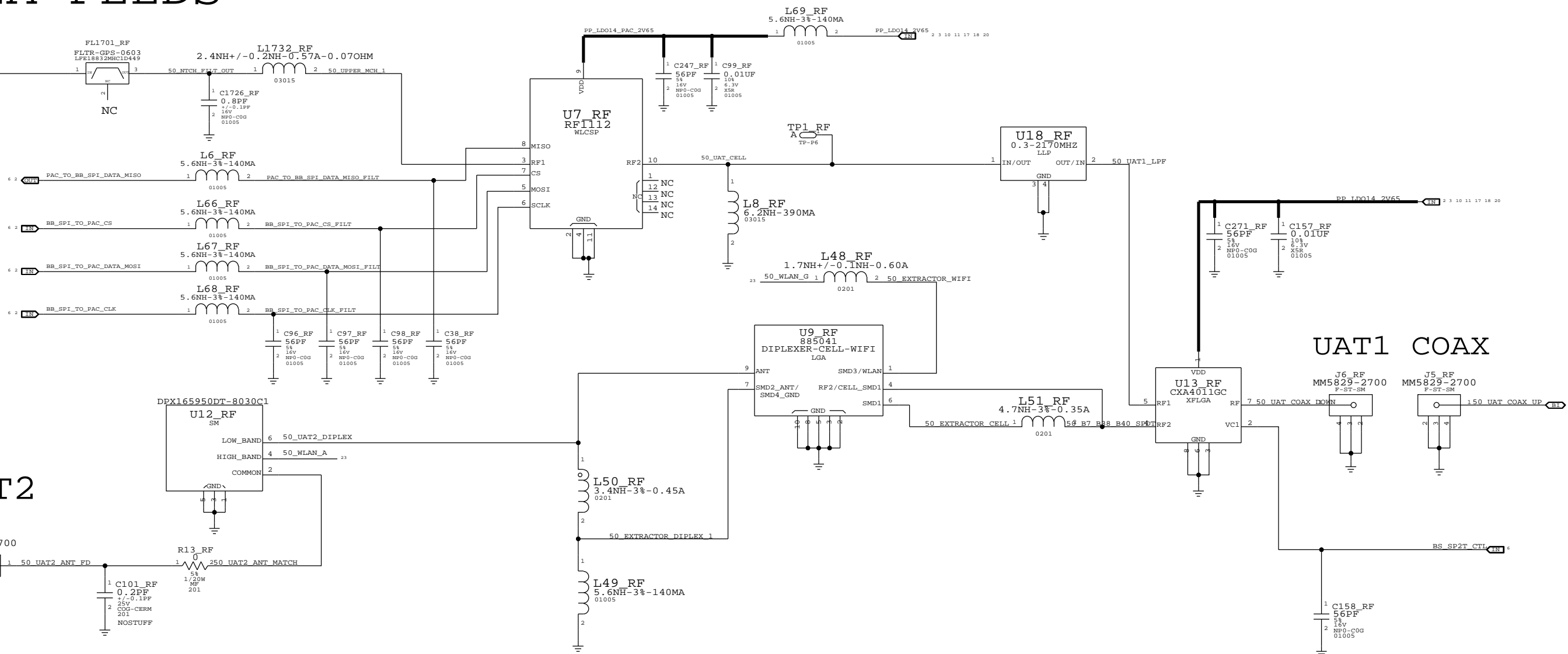


GPS

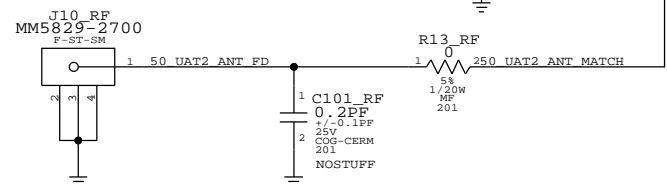


ANTENNA FEEDS

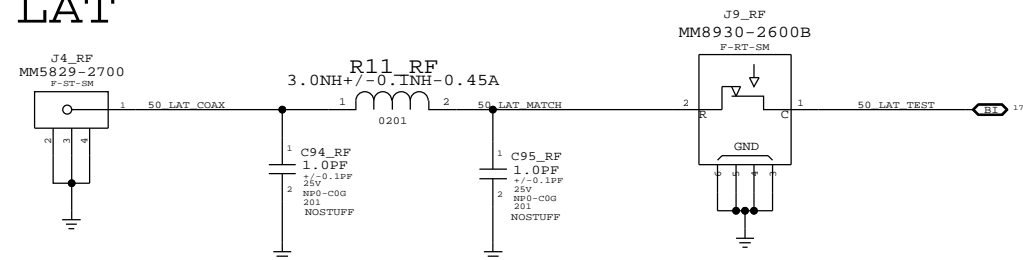
UAT1



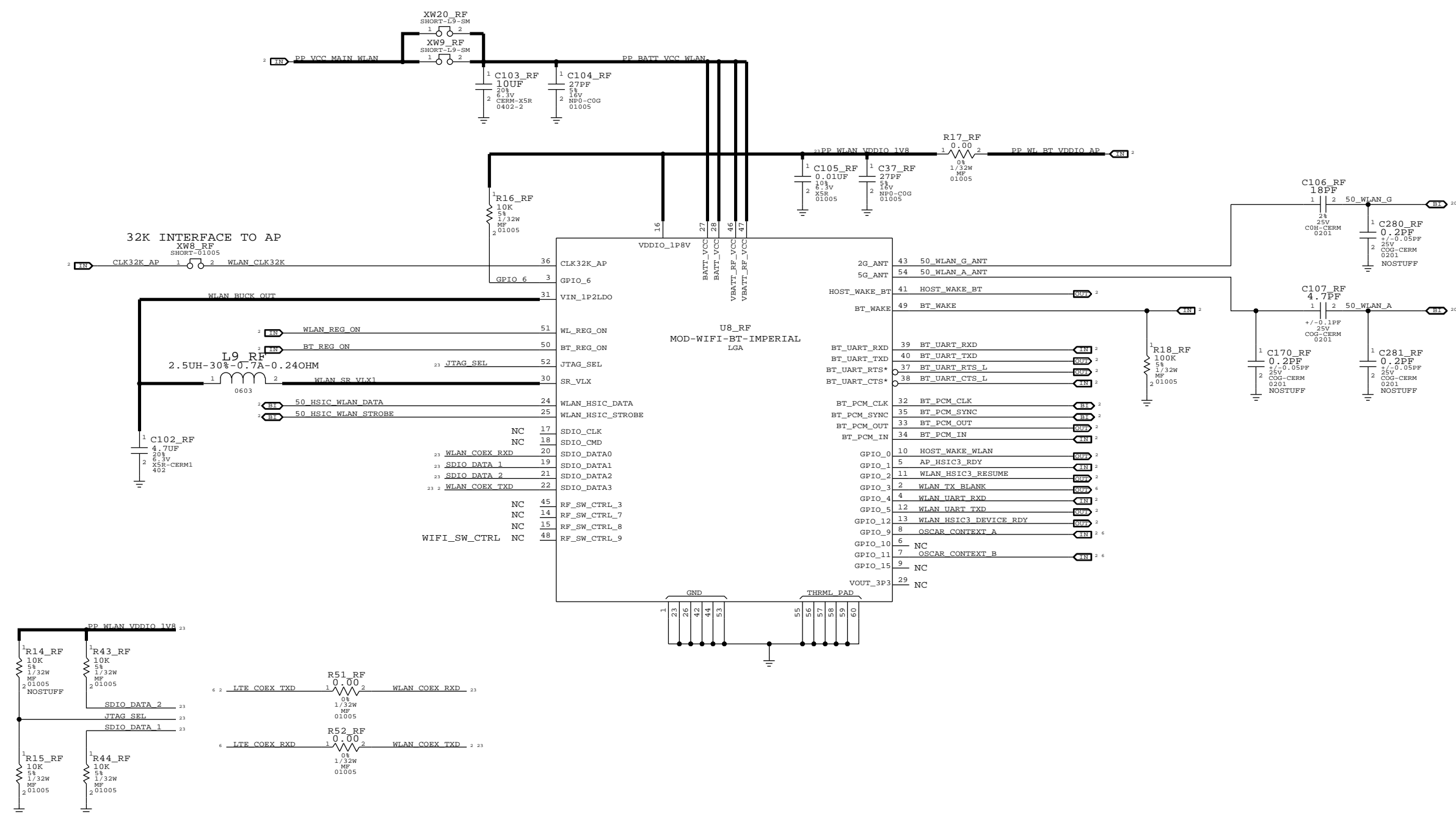
UAT2



LAT



WLAN/BT



PULL-UP ON GPIO6, SDIO_DATA_2 & PULL-DOWN ON SDIO_DATA_1 REQUIRED FOR HSIC BOOTSTRAPPING